



LAND



SEA



AIR



F40-P

Refer 1/2 Short ATR (Customization)



- IP66 Military Air Borne Mission Computer
- Intel 12th Gen Alder Lake P,i7-1265UE(10xC),4.7GHz
- Frame Grabber : 4xCH HD-SDI
- 3xDVI, 2xLAN, 2xUSB3.0, 3xCOM
- 1 x 2.5" Swappable SATA Drive
- MIL-STD 18V~36V EMI DC Input , Options for MIL-STD-704/ 461/ 1275 10V~40V DC
- Extreme Temperature -40~+60 degree
- MIL-STD 810 509 Salt Fog (Conformal Coating)
- MIL-STD 810 500.5 Low Pressure (50,000 ft) Altitude

Specifications

System

CPU	Intel® Core™ i, i7-1265UE (SoC) ,15W, 10 Core, 4.7GHz
Memory type	2 x DDR5 SODIMM up to 64GB
Chipset	Intel® SoC Integrated
Graphic	Intel® Iris Xe Graphics
BIOS	AMI UEFI BIOS
TPM	Nuvoton NPCT750AABYX TPM2.0
Power Type	18V~36V EMI DC Input , Options for 10V~36V DC- IN
Storage	1 x 2.5" Swappable SATA SSD Tray
Operating Temperature	-40° to +60° C
Dimension	134.36(W) x 177(L)x 287(H) mm

Front I/O

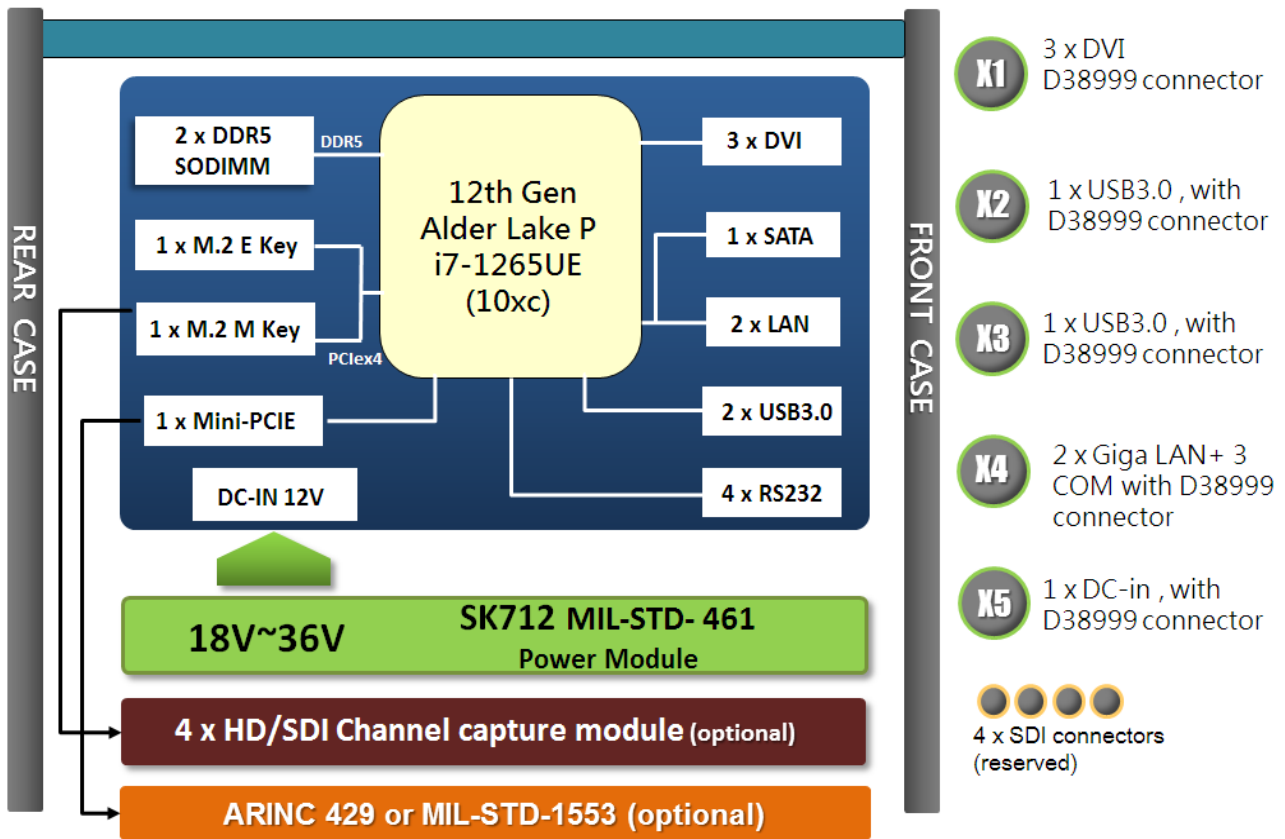
X1	3 x DVI with 100 PIN D38999 connector
X2	1 x USB3.0 with USB3.0 D38999 connector
X3	1 x USB3.0 with USB3.0 D38999 connector
X4	2 x LAN +3 x COM with 50PIN D38999 connector
X5	1 x DC IN with D38999 connector
Others	1 x 2.5" Easy swap HDD/SSD Tray 4 x SDI connectors 1 x Power button 1 x Reset button

Environmental

MIL-STD-810 Test	Method 500.5, Procedures I and II (Altitude, Operation): 12,192M, (40,000 ft) for the initial cabin altitude (18.8Kpa or 2.73 Psia) Method 500.5, Procedures III and IV (Altitude, Non-Operation): 15,240, (50,000 ft) for the initial cabin altitude (14.9Kpa or 2.16 Psia) Method 501.5, Procedure I (Storage/High Temperature) Method 501.5, Procedure II (Operation/High Temperature) Method 502.5, Procedure I (Storage/Low Temperature) Method 502.5, Procedure II (Operation/Low Temperature) Method 503.5, Procedure I (Temperature shock) Method 507.5, Procedure II (Temperature & Humidity) Method 509.7 Salt Spray (50±5)g/L
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	<p>Method 514.6, Vibration Category 24/Non-Operating (Category 20 & 24,Vibration)</p> <p>Method 514.6, Vibration Category 20/Operating (Category 20 & 24,Vibration)</p> <p>Method 516.6, Shock-Procedure V Non-Operating (Mechanical Shock)</p> <p>Method 516.6, Shock-Procedure I Operating (Mechanical Shock)</p>
Reliability	<p>No Moving Parts; Passive Cooling.</p> <p>Designed & Manufactured using ISO 9001 Certified Quality Program.</p>
MIL-STD-461	<p>CE102 basic curve, 10kHz - 30 MHz</p> <p>RE102-4, (1.5 MHz) -30 MHz - 5 GHz</p> <p>RS103, 200 MHz - 3.2 GHz, 50 V/m equal for all frequencies</p> <p>EN 61000-4-2: Air discharge: 8 kV, Contact discharge: 6kV</p> <p>EN 61000-4-3: 10V/m</p> <p>EN 61000-4-4: Signal and DC-Net: 1 kV</p> <p>EN 61000-4-5: Leads vs. ground potential 1kV, Signal und DC-Net: 0.5 kV</p> <p>CE and FCC</p>
MIL-STD-1275	<p>Steady State – 20V~33V,</p> <p>Surge Low – 18V/500ms,</p> <p>Surge High – 100V/500ms</p> <p>Emitted spikes</p> <p>Injected Voltage surges</p> <p>Emitted voltage surges</p> <p>Voltage ripple (2V)</p> <p>Voltage spikes</p> <p>Starting Operation</p> <p>Reverse polarity</p>
MIL-STD-704	<p>Load Measurements (LDC101)</p> <p>Steady State Limits for Voltage (LDC102)</p> <p>Voltage Distortion Spectrum (LDC103)</p> <p>Total Ripple (LDC104)</p> <p>Normal Voltage Transients (LDC105)</p> <p>Power Interrupt (LDC201)</p> <p>Abnormal Steady State Limits for Voltage (LDC301)</p> <p>Abnormal Voltage Transients (LDC302)</p> <p>Emergency Steady State Limits for Voltage (LDC401)</p> <p>Starting Voltage Transients (LDC501)</p> <p>Power Failure (LDC601)</p> <p>Phase Reversal (LDC602)</p>
Operating Temp.	-40 to +60°C (ambient with air flow)
Storage Temp.	-40 to +85°C
Relative Humidity	5% to 95%, non-condensing.

Block Diagram



Appearance

