



IV320-RH-K0

IP65 MISSION GPU COMPUTER



Safety Information

Electrical safety

- To prevent electrical shock hazard, disconnect the power cable from the electrical outlet before relocating the system.
- When adding or removing devices to or from the system, ensure that the power cables for the devices are unplugged before the signal cables are connected. If possible, disconnect all power cables from the existing system before you add a device.
- Before connecting or removing signal cables from the motherboard, ensure that all power cables are unplugged.
- Seek professional assistance before using an adapter or extension cord. These devices could interrupt the grounding circuit.
- Make sure that your power supply is set to the correct voltage in your area.
- If you are not sure about the voltage of the electrical outlet you are using, contact your local power company.
- If the power supply is broken, do not try to fix it by yourself. Contact a qualified service technician or your local distributor.

Operation safety

- Before installing the motherboard and adding devices on it, carefully read all the manuals that came with the package.
- Before using the product, make sure all cables are correctly connected and the power cables are not damaged. If you detect any damage, contact your dealer immediately.
- To avoid short circuits, keep paper clips, screws, and staples away from connectors, slots, sockets and circuitry.
- Avoid dust, humidity, and temperature extremes. Do not place the product in any area where it may become wet.
- Place the product on a stable surface.
- If you encounter any technical problems with the product, contact your local distributor

Statement

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- All trademarks are the properties of the respective owners.
- All product specifications are subject to change without prior notice

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Revision Date: Aug.05.2024

Revision History

Revision	Date (yyyy/mm/dd)	Changes
Version 1.0	2024/08/05	Initial release

Packing List

Item	Description	Q'ty
1	DVD(User Manual / Datasheet / Test Report)	1

Ordering Information

Model Number	Description
IV320-RH-KD	IP65 Mission GPU Computer with Intel® 13th Gen. Raptor Lake-H Core i7-13800HRE Processors / Nvidia A4500 MXM GPU, up to 64GB DDR5 SO-DIMM 5200MHz, 2 x LAN / 2 x CAN / 4 x COM(RS485) / 2 x USB3.0 / 1 x Mini DP with M12/M20 connector, 8CH 3G-SDI with BNC connector, 12~36V DC-IN, operation temperature -20~60°C

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1. CHAPTER 1: PRODUCT INTRODUCTION

1-1. Specifications

SYSTEM

CPU	Intel® 13th Raptor Lake Core i7-13800HE/HRE, 14C, 20T, 2.5/5.0GHz, 24M Cache, TDP 45W
Memory type	2 x DDR5 DIMMs Up to 64GB SO-DIMM 5200MHz
Chipset	SoC, integrated with CPU
GPU	NVIDIA® Quadro RTX A4500 - (5888 CUDA Cores)

STORAGE

HDD/SSD	Dual Reomable 2.5" SATA Tray
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FRONT I/O

X1	1 x GbE with M12 connector
X2	1 x GbE with M12 connector
X3	2 x CAN with M12 connector
X4	2 x COM(RS485) with M12 connector
X5	2 x COM(RS485) with M12 connector
X8	1 x USB3.0 with M20 connector
X9	1 x USB3.0 with M20 connector
X10	1 x Mini DP with waterproof connector
DC-IN	1 x DC-IN 12~36V with M12 connector
HDD LED	1 x HDD LED
Power Button	1 x Power Button w/Indicator LED

REAR I/O

3G-SDI	8 x 3G-SDI with BNC Connector
GND	1 x GND Screw

OPERATING SYSTEM

OS	Windows 10/ 11 64bit Enterprise, Linux by request
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PHYSICAL

Power Requirement	12V~36V DC-IN
Dimension	250 x 325 x 138 mm (W x D x H)

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Ingress Protection	IP65
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Operating Temp.	-20°C to 60°C
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Storage Temp.	-40°C to 85°C
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Relative Humidity	5% to 95%, non-condensing
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System Design	Conduction Cooing
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Standard Compliance

MIL-STD-810 (Compliance)	Method 507.5, Procedure II (Temperature & Humidity) Method 516.6 Shock-Procedure V Non-Operating (Mechanical Shock) Method 516.6 Shock-Procedure I Operating (Mechanical Shock) Method 514.6 Vibration Category 24/Non-Operating (Category 20 & 24, Vibration) Method 514.6 Vibration Category 20/Operating (Category 20 & 24, Vibration) Method 501.5, Procedure I (Storage/High Temperature) Method 501.5, Procedure II (Operation/High Temperature) Method 502.5, Procedure I (Storage/Low Temperature) Method 502.5, Procedure II (Operation/Low Temperature) Method 503.5, Procedure I (Temperature shock)
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Reliability	No Moving Parts; Passive Cooling. Designed & Manufactured using ISO 9001 / 2000 Certified Quality Program.
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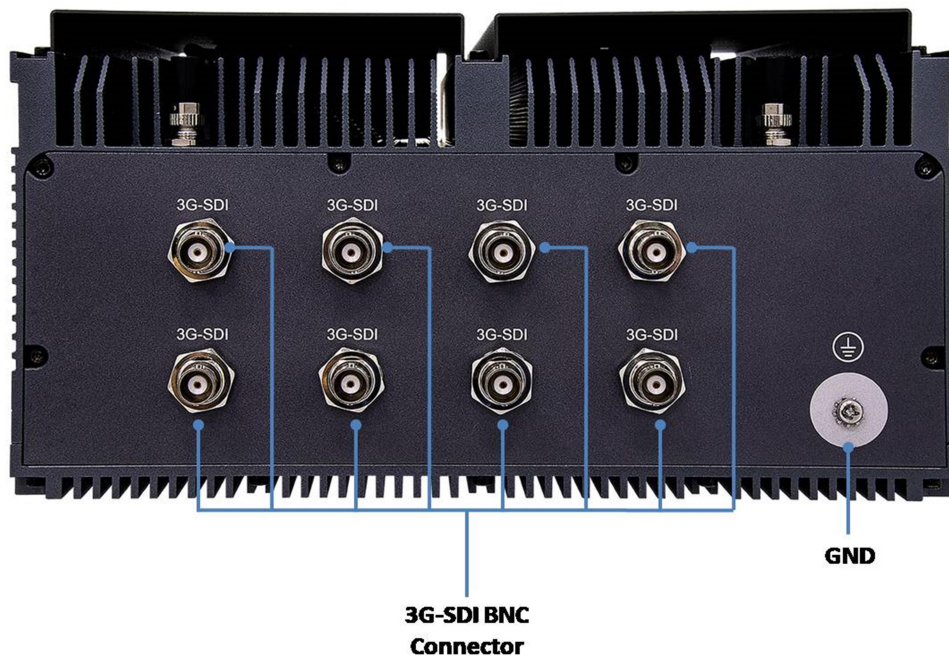
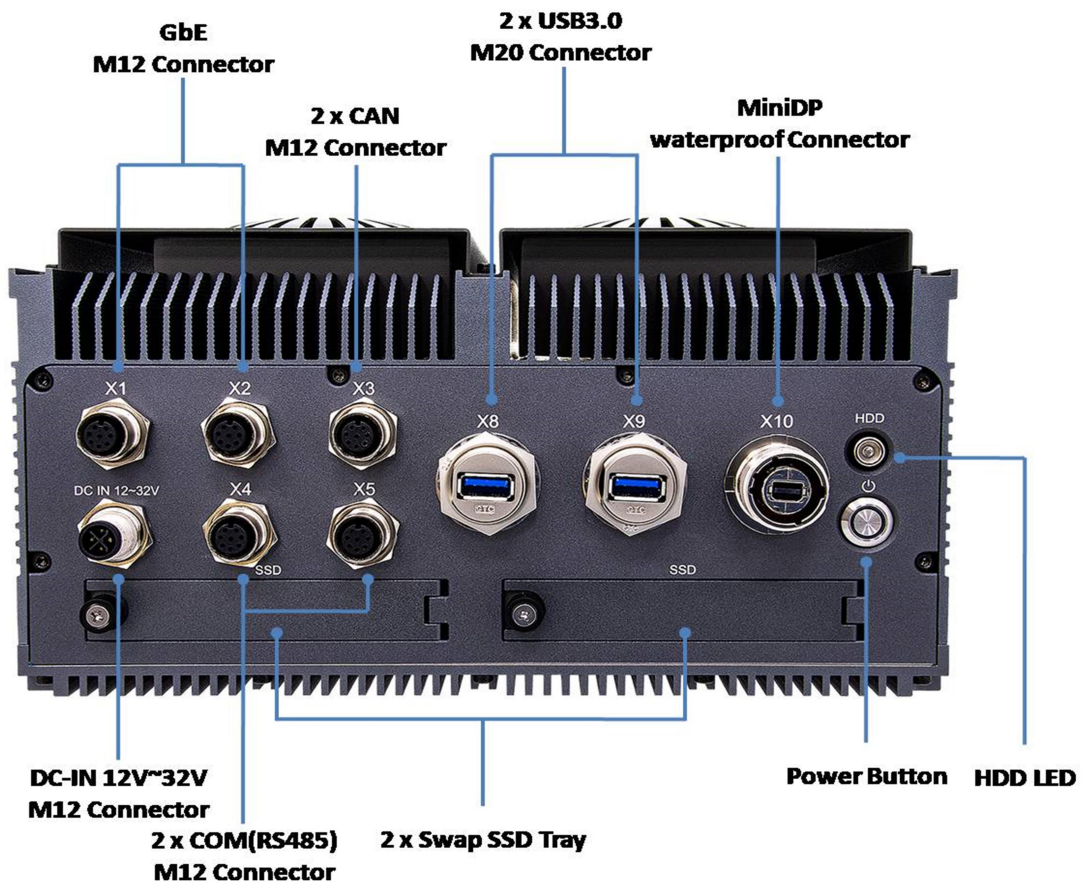
CE/FCC (Compliance)	CE/FCC Compliance
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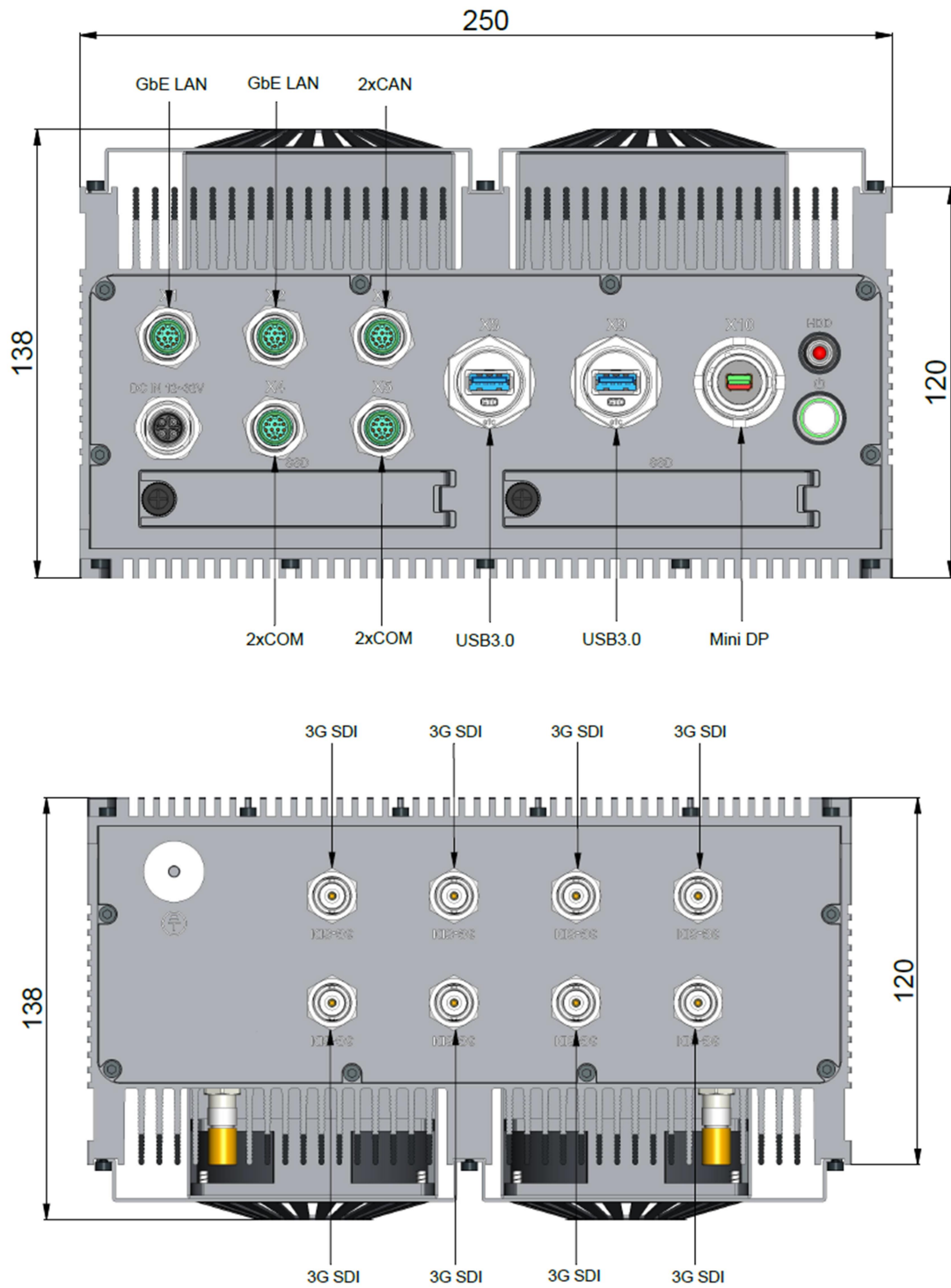
1-2. Appearance



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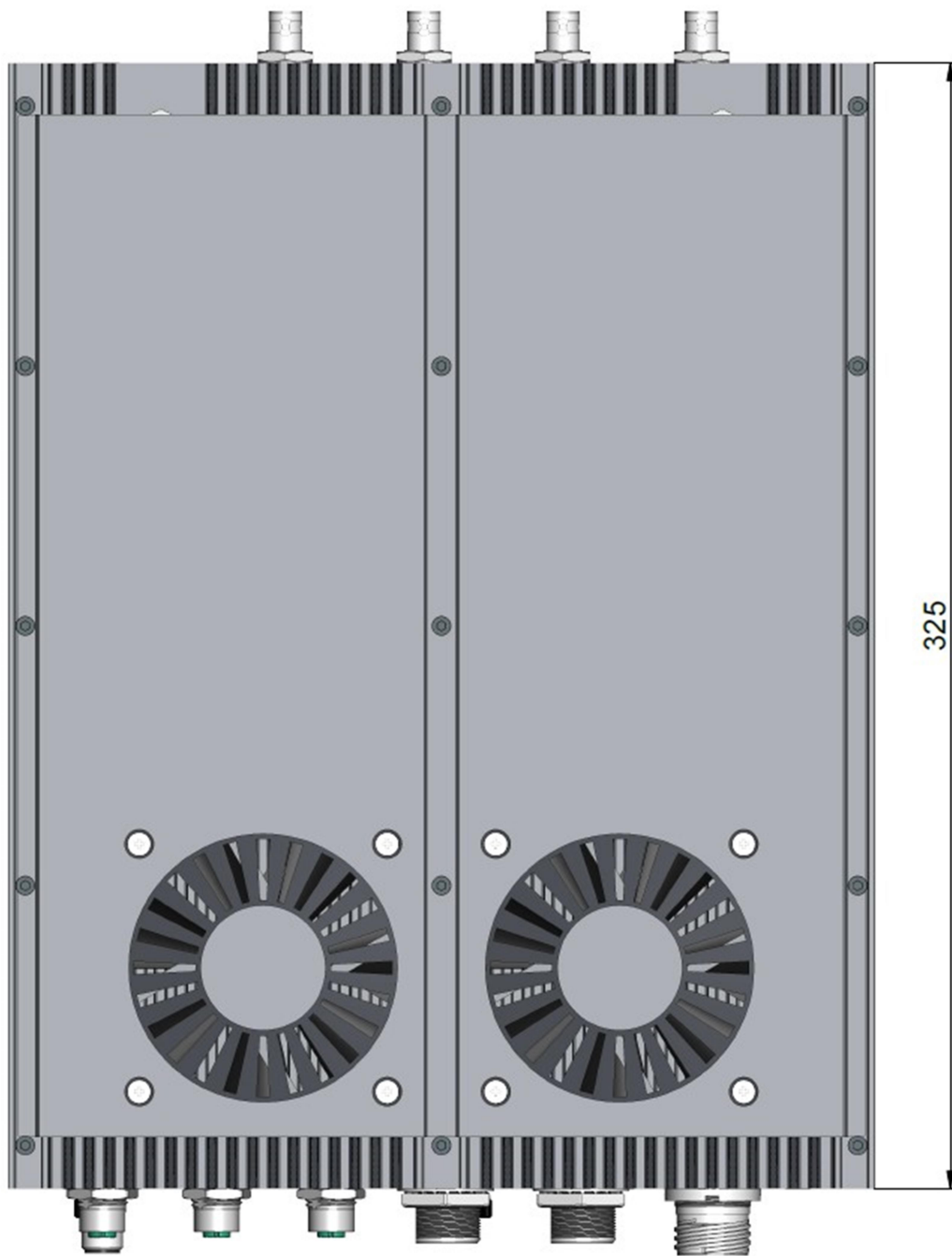
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1.3 Dimension



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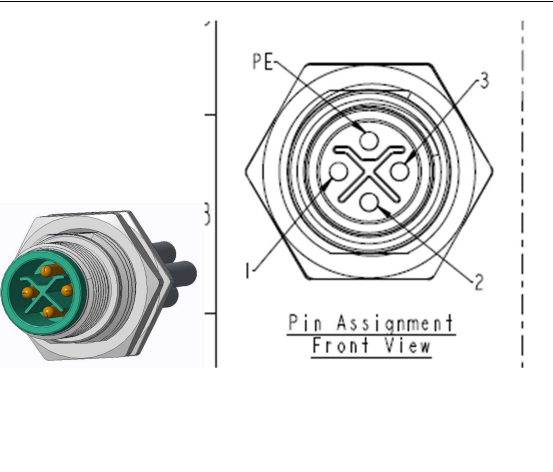


2 CHAPTER 2: M12/M20/BNC PIN DEFINE

2.1. Connector & Pin Definitions

DC-IN Power


Pin	Function
1	VIN+
2	VIN+
3	VIN-
PE	VIN-



Pin Assignment
Front View

Dual RS485 with M12 Connector

Pin	Function (RS485)
1	D0+
2	D0-
3	N/A
4	GND
5	D1+
6	D1-
7	N/A
8	GND



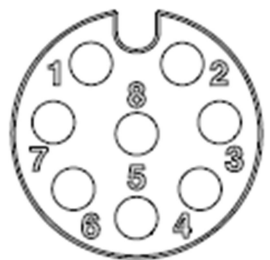
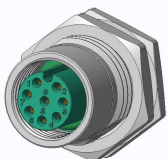
C1
Pin Assignments
Front View

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Dual CANBUS with M12 Connector

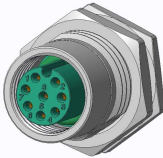
Pin	Function (CANBUS)
1	CAN-H
2	CAN-L
3	N/A
4	GND
5	CAN-H
6	CAN-L
7	N/A
8	GND

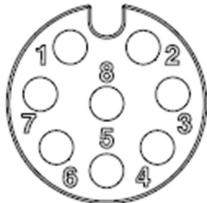


C1
Pin Assignments
Front View

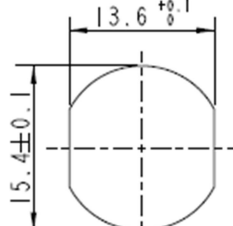
LAN with M12 Connector

Pin	Function (CANBUS)	RJ45	Cable Color
1	D1+	1	White/Orange
2	D1-	2	Orange
3	D2+	3	White/Green
4	D2-	6	Green
5	D3+	5	White/Blue
6	D3-	4	Blue
7	D4+	7	White/Brown
8	D4-	8	Brown
	SHELL	SHELL	





C1
Pin Assignments
Front View



C1
Recommended
Panel Cut-Out

3 CHAPTER 3: AMI BIOS UTILITY

This chapter provides users with detailed descriptions on how to set up a basic system configuration through the AMI BIOS setup utility.

3.1 Starting

To enter the setup screens, perform the following steps:

- Turn on the computer and press the key immediately.
- After the key is pressed, the main BIOS setup menu displays. Other setup screens can be accessed from the main BIOS setup menu, such as the Chipset and Power menus.

3.2 Navigation Keys

The BIOS setup/utility uses a key-based navigation system called hot keys. Most of the BIOS setup utility hot keys can be used at any time during the setup navigation process. Some of the hot keys are <F1>, <F10>, <Enter>, <ESC>, and <Arrow> keys.

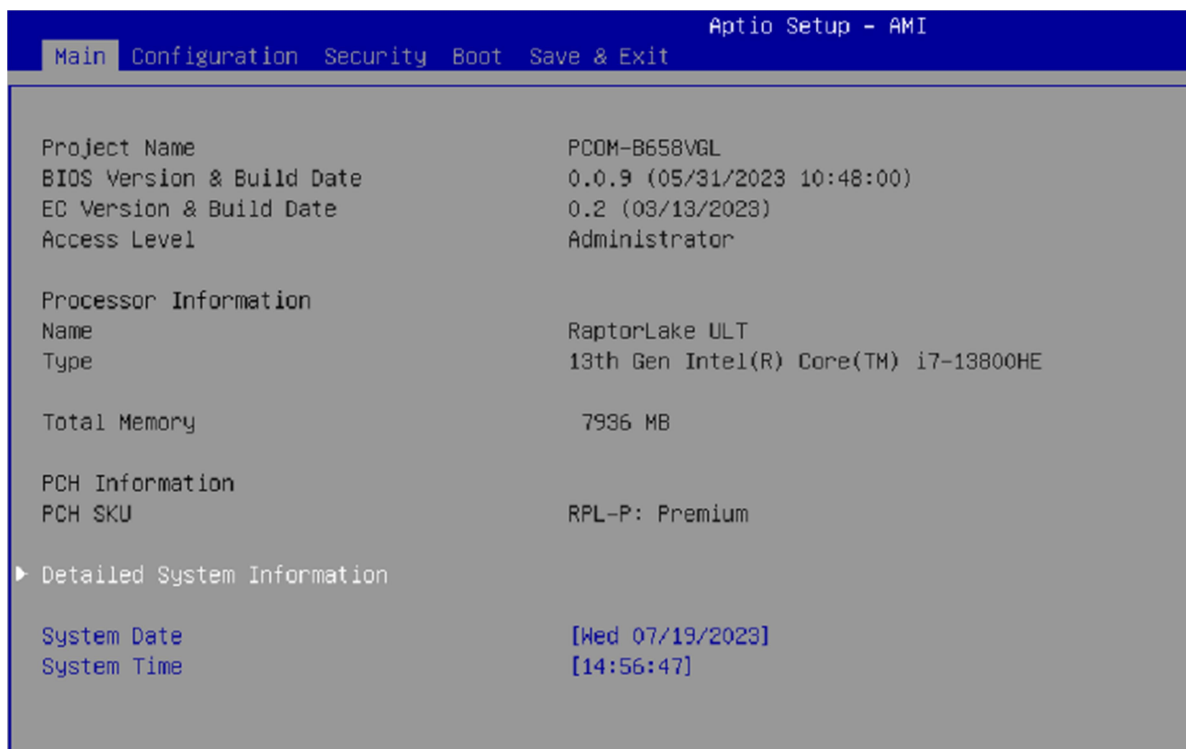


Some of the navigation keys may differ from one screen to another.

Left/Right	The Left and Right <Arrow> keys moves the cursor to select a menu.
Up/Down	The Up and Down <Arrow> keys moves the cursor to select a setup screen or sub-screen.
+– Plus/Minus	The Plus and Minus <Arrow> keys changes the field value of a particular setup setting.
Tab	The <Tab> key selects the setup fields.
F1	The <F1> key displays the General Help screen.
F10	The <F10> key saves any changes made and exits the BIOS setup utility.
Esc	The <Esc> key discards any changes made and exits the BIOS setup utility.
Enter	The <Enter> key displays a sub-screen or changes a selected or highlighted option in each menu.

3.3 Main

Use this menu for basic system configurations, such as time, date etc.



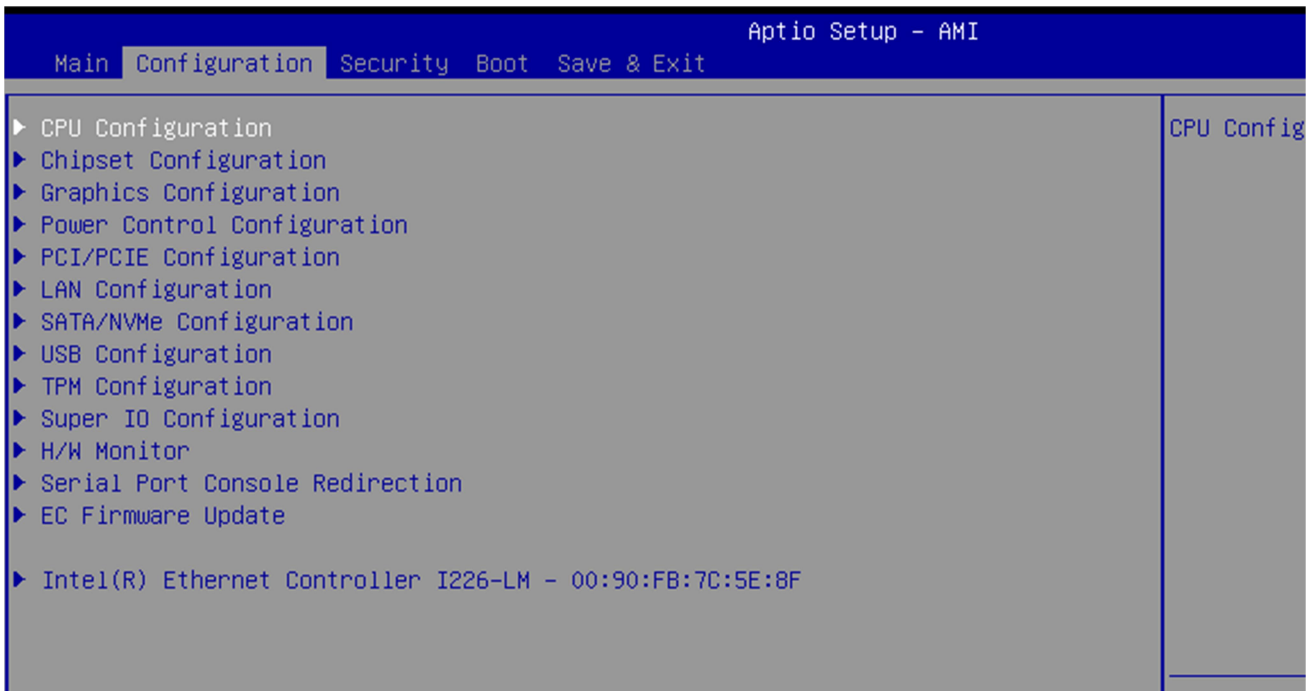
Feature	Description	Options
Detailed System Information		
System Date	The date format is <Day>, <Month> <Date> <Year>. Use [+] or [-] to configure system Date.	
System Time	The time format is <Hour> <Minute> <Second>. Use [+] or [-] to configure system Time.	

3.4 Configuration

Use this menu to set up the items of special enhanced features.

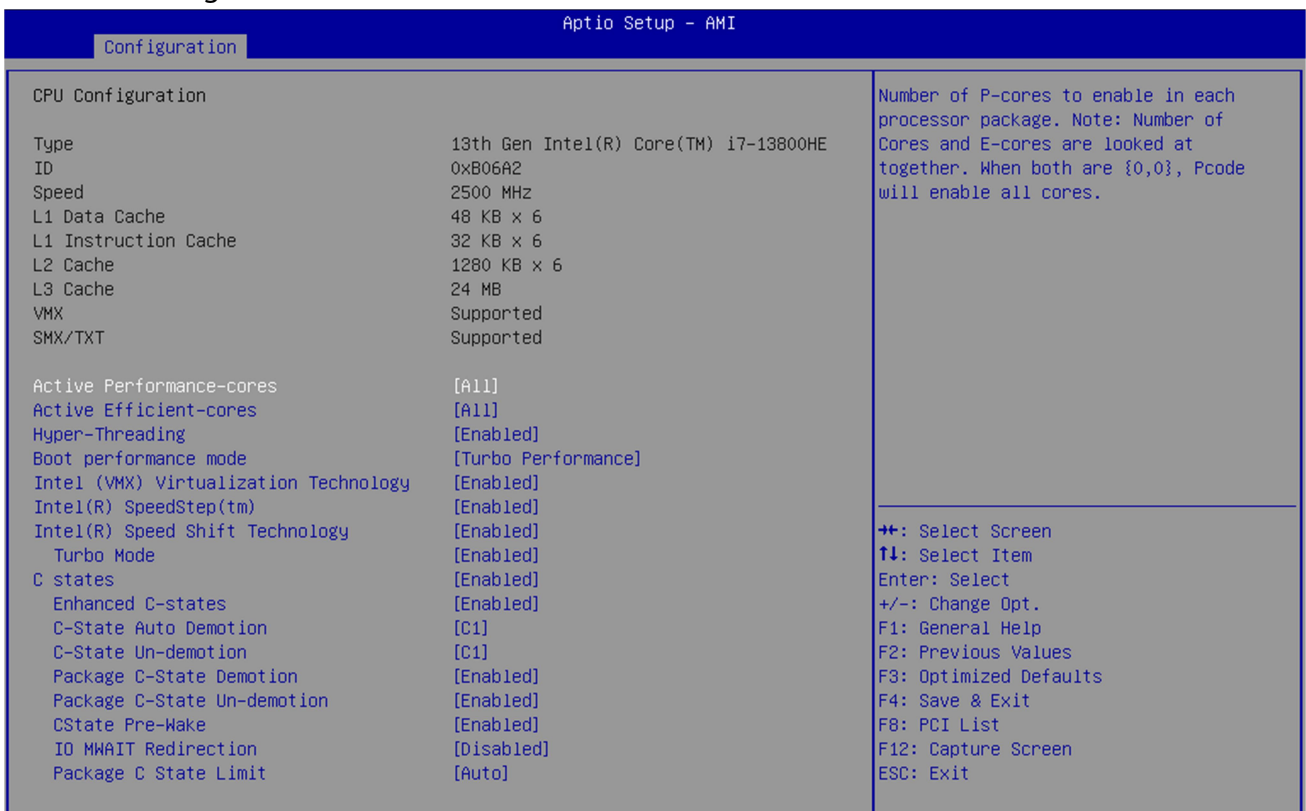
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● CPU Configuration

CPU Configuration Parameters



Feature	Description	Options
Active Performance- cores	Number of P-cores to enable in each processor package. Note: Number of Cores and E-cores are looked at together. When both are {0,0}, Pcode will enable cores	★ All, 5, 4, 3, 2, 1

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Active Efficient-cores	Number of E-cores to enable in each processor package. Note: Number of Cores and E-cores are looked at together. When both are {0,0}, Pcode will enable cores	★ All, 7, 6, 5, 4, 3, 2, 1, 0
Hyper-Threading	Enabled or Disabled Hyper-Threading Technology.	★ Enabled, Disabled
Boot performance mode	Select the performance state that the BIOS will set starting from reset vector	Max Battery, Max Non-Turbo Performance ★ Turbo Performance,
Intel (VMX) Virtualization Technology	When enabled, a VMM can utilize the additional hardware capabilities provided by Vander pool Technology.	Disabled, ★ Enabled
Intel® Speed Step™	Allows more than two frequency ranges to be supported.	Disabled, ★ Enabled
Intel® Speed Shift Technology	Enable/Disable Intel® Speed Shift Technology support. Enabling will expose the CPPC v2 interface to allow for hardware controlled P-states	Disabled, ★ Enabled
Turbo Mode	Enable/Disable processor Turbo Mode (requires Intel Speed Step or Intel Speed Shift to be available and enabled)	Disabled, ★ Enabled
C states	Enable/disable CPU Power Management. Allows CPU to go to C states It's not 100% utilized	Disabled, ★ Enabled
Enhanced C-States	Enable/disable C1E. When enabled, CPU will switch to minimum speed when all cores enter C-State.	Disabled, ★ Enabled
C-State Auto Demotion	Configure C-State Auto Demotion	Disable, C1 ,C3 , ★ C1 and C3
C-State Un-demotion	Configure C-State Un-demotion	Disable, C1 ,C3 , ★ C1 and C3
Package C State Demotion	Package C-State Demotion	★ Disabled, Enabled
Package C State Un-demotion	Package C-State Un-demotion	★ Disabled, Enabled
CState Pre-Wake	Disable – Sets bit 30 of POWER_CTL MSR(0x1FC) to 1 to disable the Cstate Pre-Wake	Disabled, ★ Enabled
IO MWAIT Redirection	When set, will map IO_read instructions sent to IO registers PMG_IO_BASE_ADDRBASE+offset to MWAIT(offset)	★ Disabled, Enabled
Package C State Limit	Maximum Package C State Limit Setting. Cpu Default: Leaves to Factory default value. Auto: Initializes to deepest available Package C States Limit	★ Auto,C0/C1,C2,C3,C6,C7, C7S,C8,C9,C10,Cpu Default,

● Chipset Configuration Configuration Chipset feature

Configuration		Aptio Setup - AMI
Chipset Configuration		VT-d capability
VT-d	[Enabled]	
Above 4GB MMIO BIOS assignment	[Disabled]	
HD Audio	[Enabled]	
Port 80h Redirection	[LPC Bus]	
Me FW Image Re-Flash	[Disabled]	

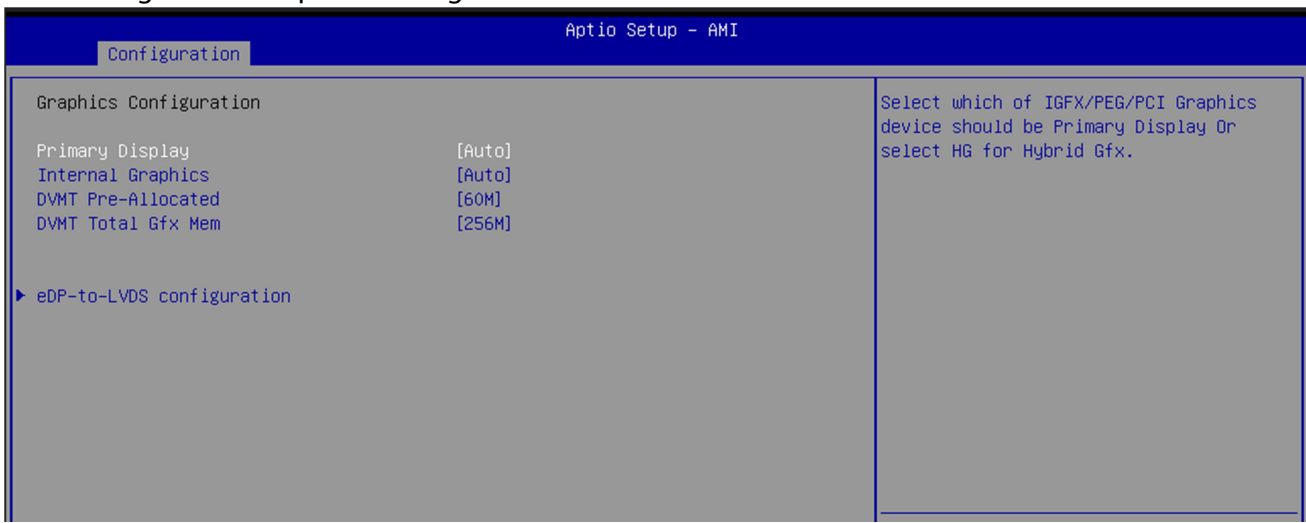
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Feature	Description	Options
VT-d	VT-d Capability	★ Enabled ,Disabled
Above 4GB MMIO BIOS assignment	Enable/Disable above 4GB MemoryMappedIO BIOS assignment This is enabled automatically when Aperture Size is set to 2048MB	★ Disabled, Enabled
HD Audio	Control Detection of the HD-Audio device. Disabled= HAD will be unconditionally disabled Enabled= HAD will be unconditionally enabled.	★ Enabled ,Disabled
Port 80h Redirection	Control where the Port 80h cycles are sent	★ LPC Bus, PCIE Bus
Me FW Image Re-Flash	Enable/Disable Me FW Image Re-Flash function	★ Disabled, Enabled

● **Graphics Configuration**

Configuration Graphics Settings



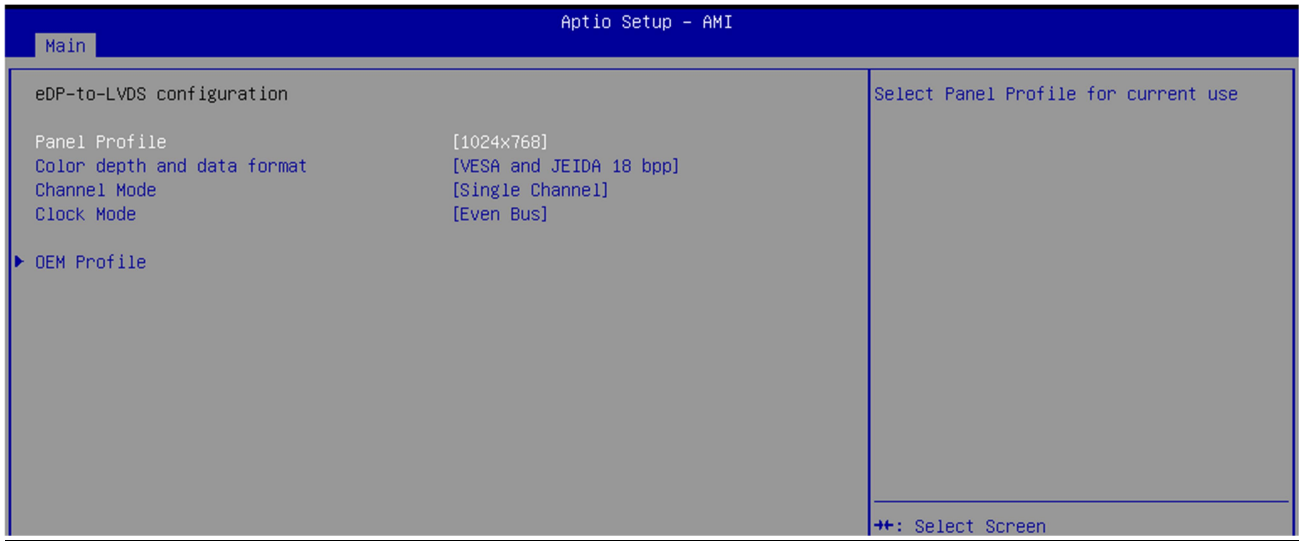
Feature	Description	Options
Primary Display	Select which of IGFX/PEG/PCI Graphics device should be Primary Display Or select SG for Switchable Gfx.	★ Auto, IGFX, PEG, PCI
Internal Graphics	Keep IGFX enable based on the setup options.	★ Auto, Disable, Enable
DVMT Pre-Allocated	Select DVMT 5.0 Pre-Allocated (Fixed) Graphics Memory size used by the Internal Graphics Device.	0M,32M,64M,4M,8M,12M,16M,20M,24M,28M, 32M/F7,36M,40M,44M,48M,52M,56M,★ 60M
DVMT Total Gfx Mem	Select DVMT5.0 Total Graphic Memory size used by the Internal Graphics Device	★ 256M, 128M, MAX

● **eDP-to-LVDS configuration**

eDP-to-LVDS

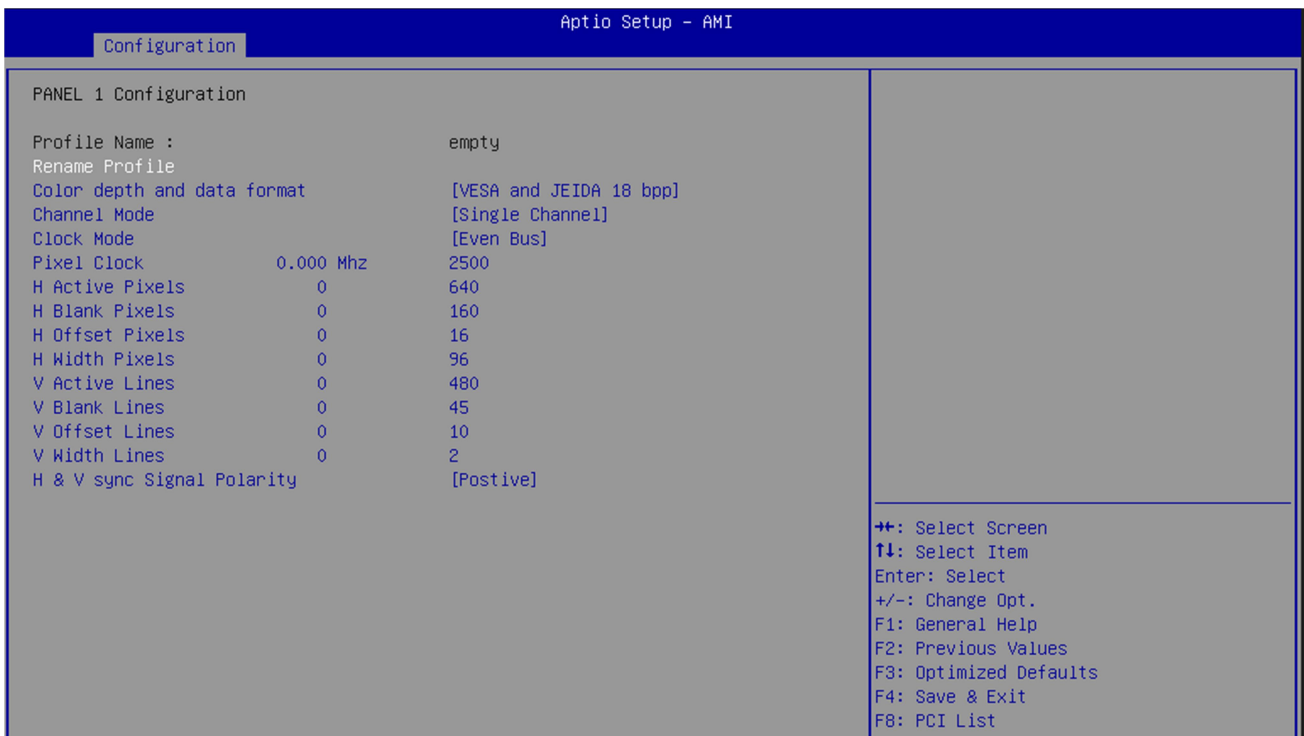
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Feature	Description	Options
Panel Profile	Select Panel Profile for current use.	★ 1024x768,640x480,800x480,800x600,1280x800 1280x1024,1366x768,1440x900,1920x1080,OEM Profile
Color depth and data format	Select Color depth and data format	★ VESA and JEIDA 18 bpp, VESA 24 bpp, JEIDA 24 bpp
Channel Mode	Select LVDS Channel Mode	★ Single Channel, Dual Channel
Clock Mode	Select clock output for LVDS.	★ Even Bus, Odd Bus, Both Buses

● OEM Profile PANEL 1 Configuration



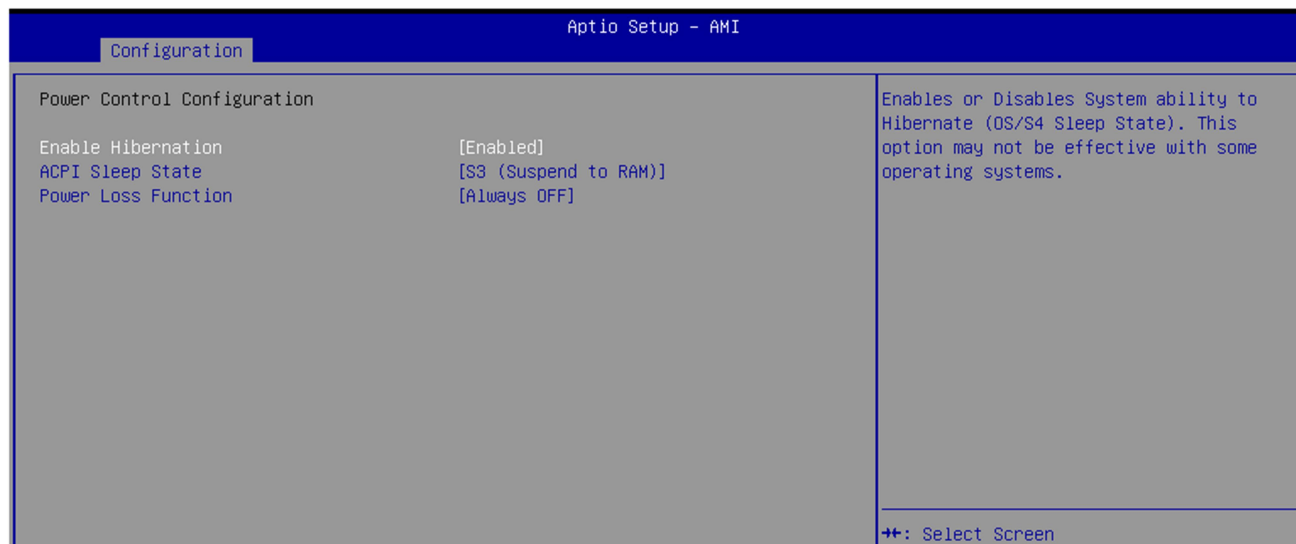
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Feature	Description	Options
Color depth and data format	Select Color depth and data format	★ VESA and JEIDA 18 bpp, VESA 24 bpp, JEIDA 24 bpp
Channel Mode	Select LVDS Channel Mode	★ Single Channel, Dual Channel
Clock Mode	Select clock output for LVDS.	★ Even Bus, Odd Bus, Both Buses
Pixel Clock	Pixel Clock(10Khz)	★ 2500
H Active Pixels	H Active Pixels (Pixel)	★ 640
H Blank Pixels	H Blank Pixels (Pixel)	★ 160
H Offset Pixels	H Offset Pixels (Pixel)	★ 16
H Width Pixels	H Width Pixels (Pixel)	★ 96
V Active Lines	V Active Lines (Line)	★ 480
V Blank Lines	V Blank Lines (Line)	★ 45
V Offset Lines	V Offset Lines (Line)	★ 10
V Width Lines	V Width Lines (Line)	★ 2
H&V sync Signal Polarity	Flag: 0x1E Signal Polarity is Postive 0x18 Signal Polarity is Non-Postive	★ Postive, Non-Postive

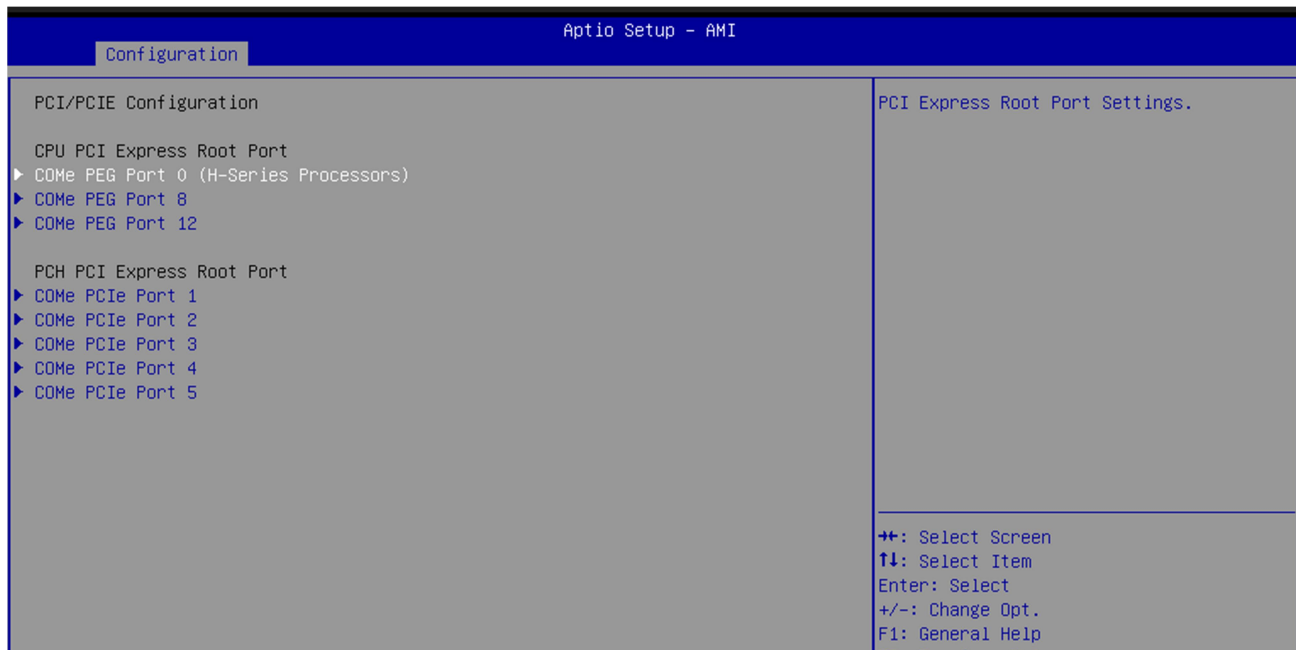
● Power Control Configuration

System Power Control Configuration Parameters

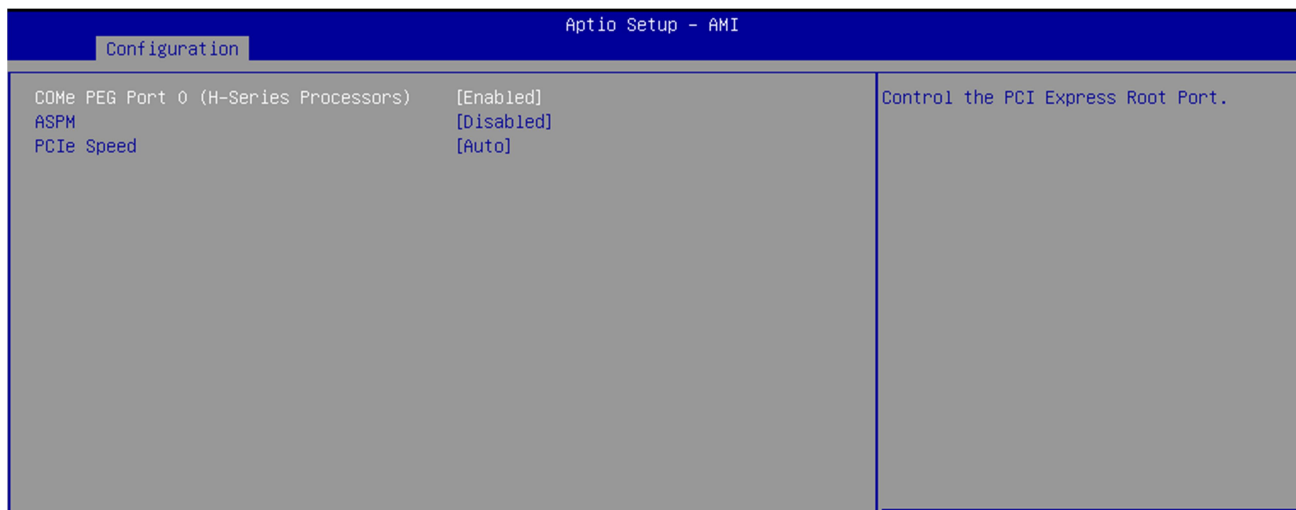


Feature	Description	Options
Enable Hibernation	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may be not effective with some operating system	Disabled, ★ Enabled
ACPI Sleep State	Select the highest ACPI sleep state the system will enter when the SUSPEND button is pressed.	Suspend Disabled , ★ S3 (Suspend to RAM)
Power Loss Function	Control SIO Power Loss Function. ON is always ON, OFF is always OFF, Last state will depends on last power state	★ Always OFF, Always ON, Last State,

- **PCI/PCIE Configuration**
PCI, PCI-X and PCI Express Settings



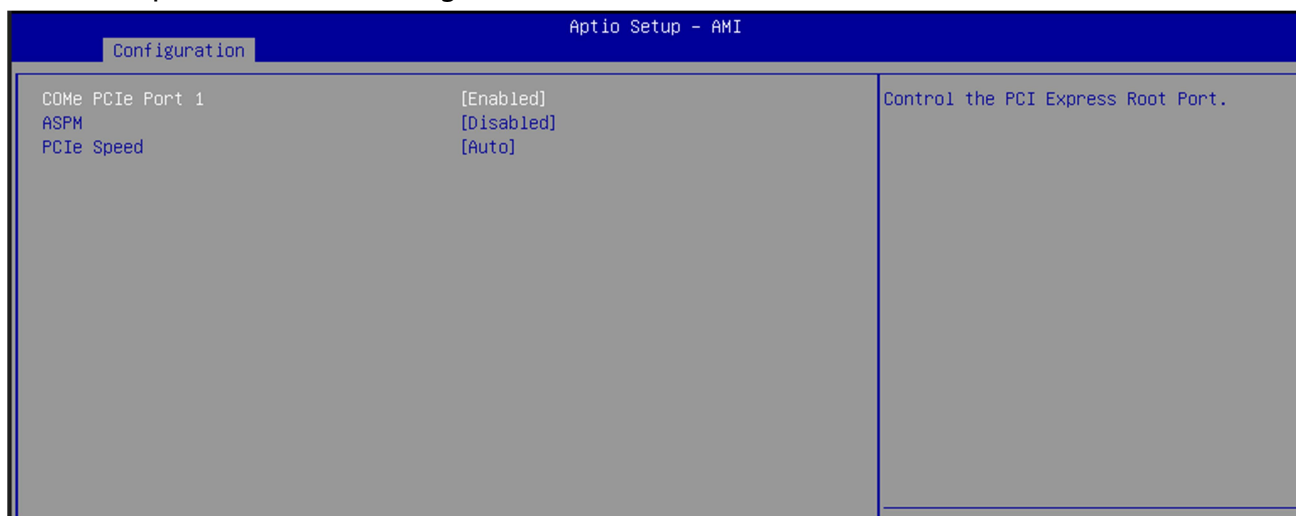
- **COMe PEG Port 0, 8, 12**
PCI Express Root Port Settings



Feature	Description	Options
COMe PEG Port 0,8, 12	Control the PCI Express Root Port.	Disabled, ★ Enabled
ASPM	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO - BIOS auto configure DISABLE – Disables ASPM	★ Disabled, L0s, L1, L0sL1, Auto
PCIe Speed	Configure PCIe Speed	★ Auto, Gen1, Gen2, Gen3

● PCH PCI Express Root Port 1~5

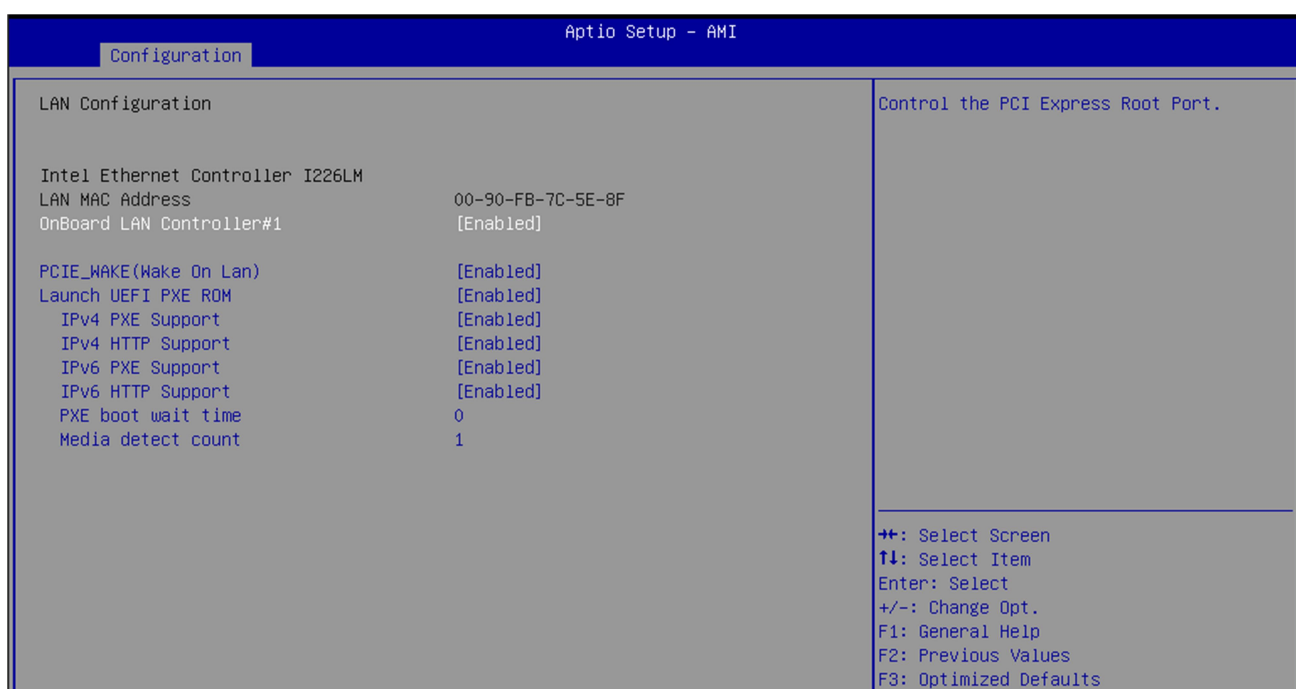
PCI Express Root Port Settings



Feature	Description	Options
COMe PCIe Port 1~5	Control the PCI Express Root Port.	Disabled, ★ Enabled
ASPM	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO - BIOS auto configure DISABLE – Disables ASPM	★ Disabled, L0s, L1, L0sL1, Auto
PCIe Speed	Configure PCIe Speed	★ Auto, Gen1, Gen2, Gen3

● LAN Configuration

Configuration On Board LAN Device



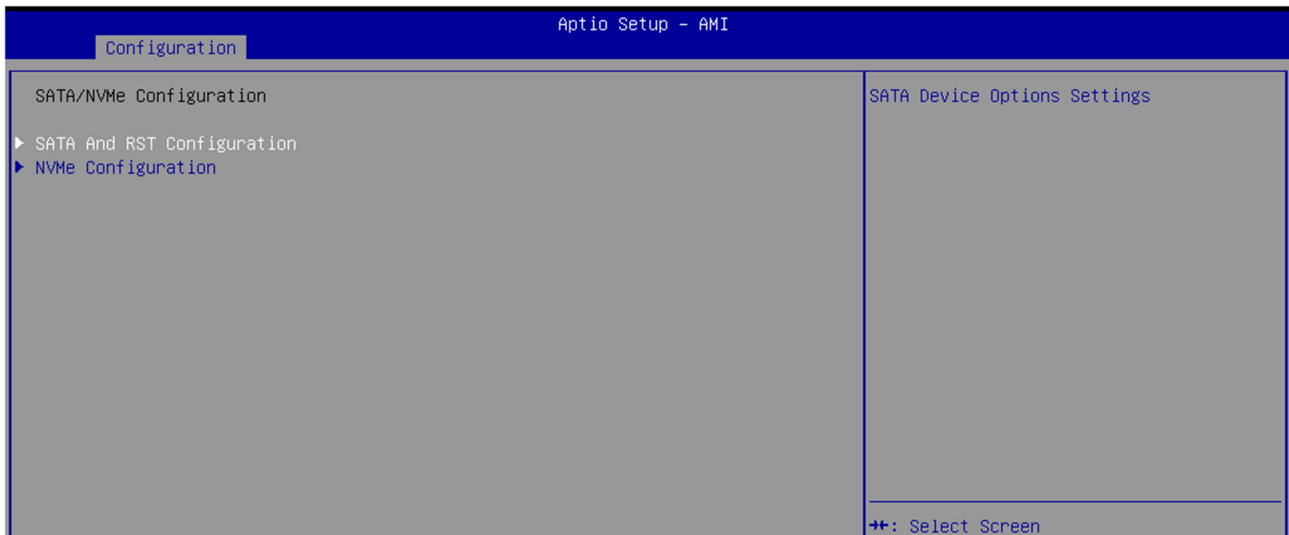
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Feature	Description	Options
Onboard LAN Controller #1	Enable/Disable onboard NIC	★ Enabled , Disabled
PCIE_WAKE (Wake on LAN)	Control PCIE wake# pin for Wake On Lan function	★ Enabled , Disabled
Launch UEFI PXE ROM	Enable/Disable UEFI Network Stack	★ Disabled, Enabled
Launch UEFI PXE ROM[Enable]		
Ipv4 PXE Support	Enable/Disable Ipv4 PXE boot support.	Disabled, ★ Enabled
Ipv4 HTTP Support	Enable/Disable Ipv4 HTTP boot support. If disable, IPv4 HTTP boot support will not be available.	Disabled, ★ Enabled
Ipv6 PXE Support	Enable/Disable Ipv6 PXE boot support. If disable, IPv6 PXE boot support will not be available.	Disabled, ★ Enabled
Ipv6 HTTP Support	Enable/Disable Ipv6 HTTP boot support. If disable, IPv6 HTTP boot support will not be available.	Disabled, ★ Enabled
IPSEC Certificate	Support to Enable/Disable IPSEC certificate for Ikev	Disabled, ★ Enabled
PXE boot wait time	Wait time in seconds to press ESC key to abort the PXE boot. Use either +/- or numeric keys to set the values	★ 0
Media detect count	Number of times the presence of media will be checked. Use either +/- or numeric keys to set the values.	★ 1

● SATA Configuration

SATA/NVMe Device Options Settings

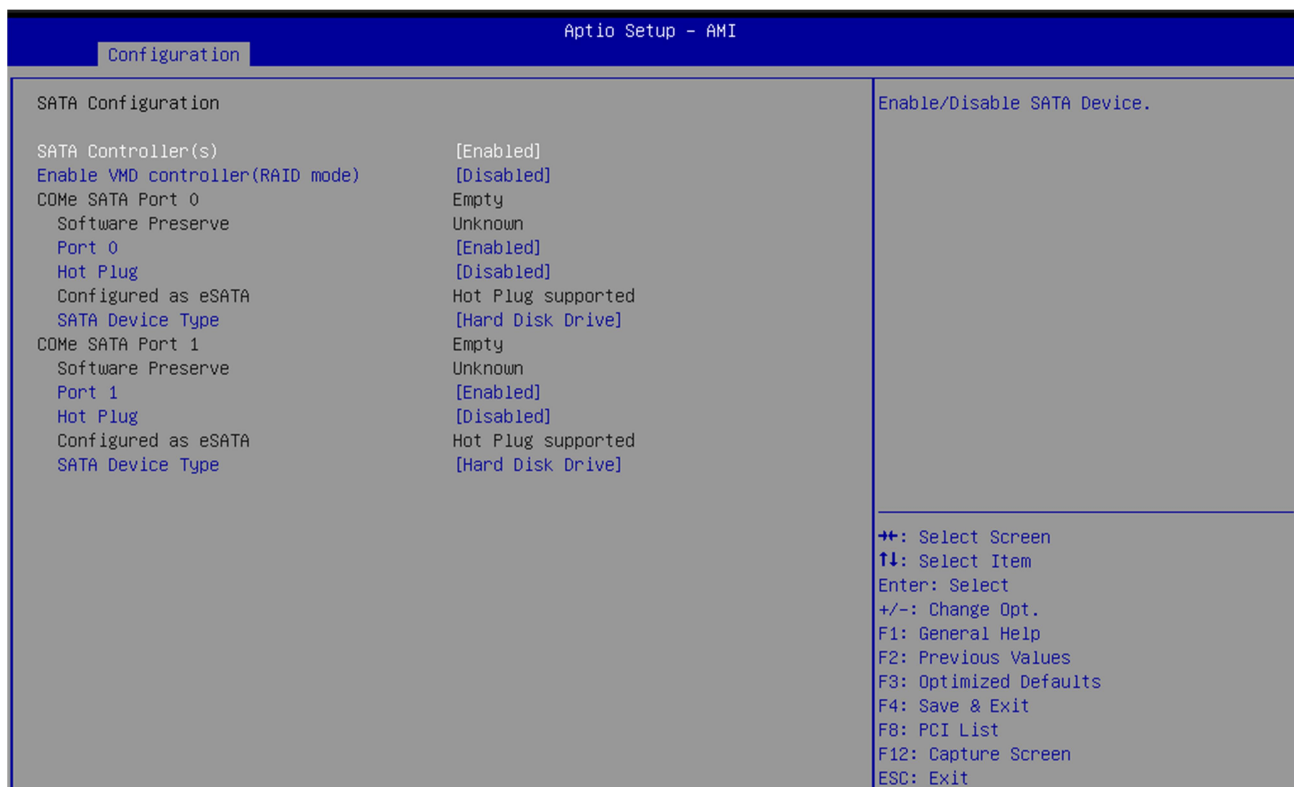


Feature	Description	Options
SATA And RST Configuration	SATA Device Options Settings	SATA And RST Configuration
NVMe Configuration	NVMe Device Options Settings	NVMe Configuration

● SATA And RST Configuration

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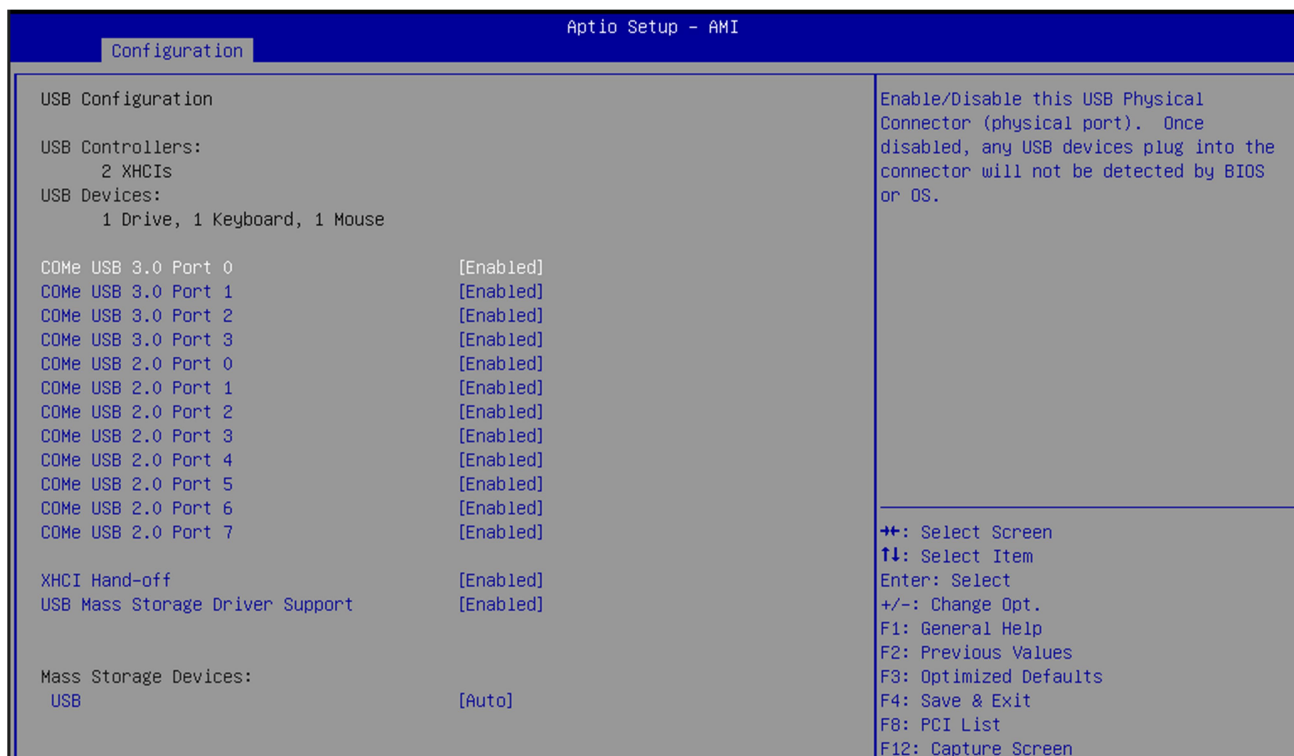
Feature	Description	Options
SATA Controller(s)	Enable/Disable the SATA Device.	★ Enabled , Disabled
Enable VMD controller(RAID Mode)	Enable/Disable to VMD controller	★ Disabled, Enabled
COMe SATA Port 0~1		
Port 0~1	Enable or Disable SATA Port	★ Enabled ,Disabled
Hot Plug	Designates this port as Hot Pluggable	★ Disabled, Enabled
SATA Device Type	Identify the SATA port is connected to Solid State Drive or Hard Disk Drive	★ Hard Disk Drive,Solid State Drive

● USB Configuration

USB Configuration Parameters

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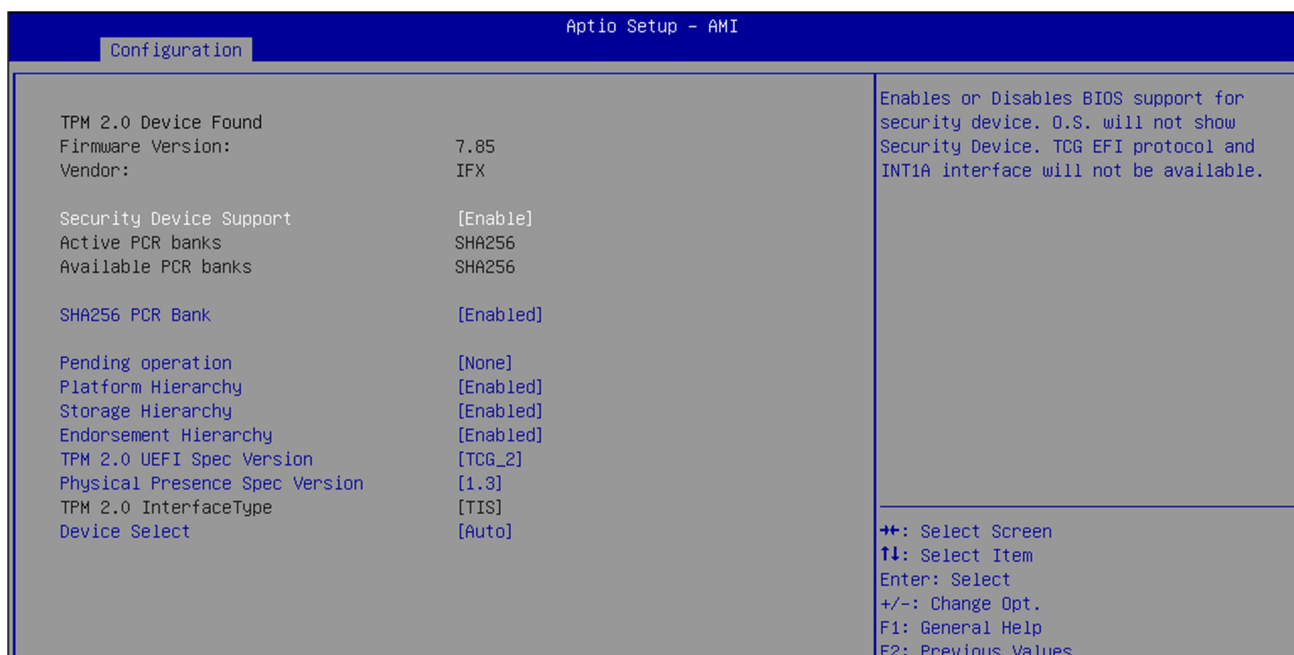


Feature	Description	Options
COMe USB 3.0 Port #0~7	Enable/Disable this USB Physical Connector (physical port). Once disabled, any USB devices plug into the connector will not be detected by BIOS or OS	★ Enabled ,Disabled
XHCI Hand-off	This is a workaround for Oses without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver	★ Enabled ,Disabled
USB Mass Storage Driver Support	Enable/Disable USB Mass Storage Driver Support	★ Enabled ,Disabled
USB	Mass storage device emulation type. 'AUTO' enumerates devices according to their media format. Optical drives are emulated as 'CDROM', drives with no media will be emulated according to a drive type	★ Auto, Floppy, Forced FDD, Hard Disk, CD-ROM
COMe USB 3.0 Port #0~7	Enable/Disable this USB Physical Connector (physical port). Once disabled, any USB devices plug into the connector will not be detected by BIOS or OS	★ Enabled ,Disabled

- **TPM Configuration**
Trust Computing Settings

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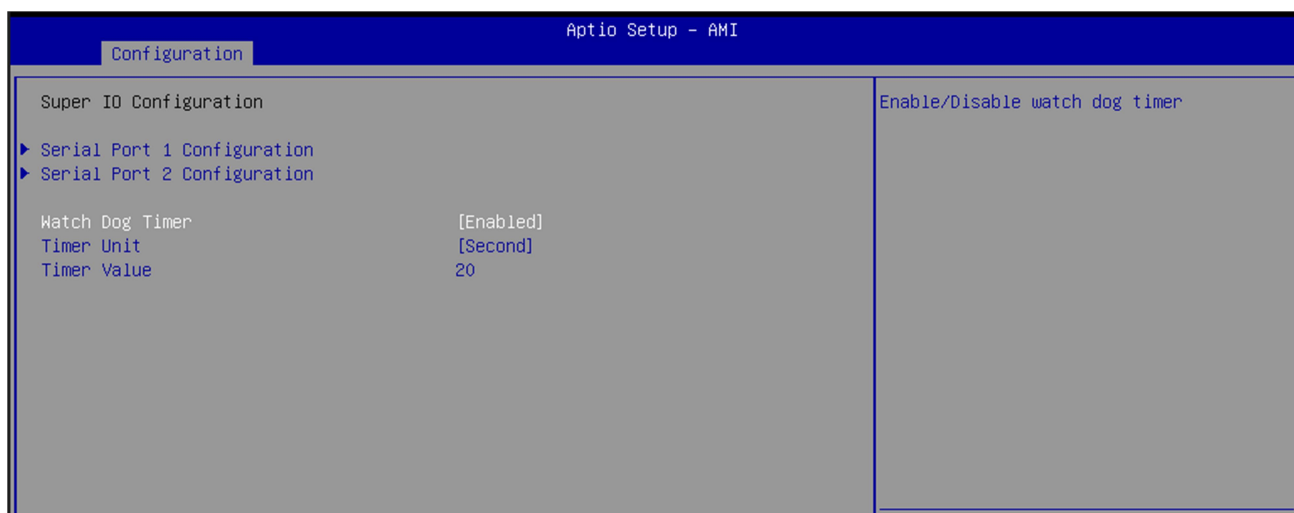
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Feature	Description	Options
Security Device Support	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A Interface will not be available.	Disabled, ★ Enabled
SHA256 PCR Bank	Enables or Disables SHA256 PCR Bank	Disabled, ★ Enabled
Pending operation	Schedule an Operation for the Security Device. Note: Your Computer will reboot during restart in order to change State of Security Device	★ None, TPM Clear
Platform Hierarchy	Enables or Disables Platform Hierarchy	Disabled, ★ Enabled
Storage Hierarchy	Enables or Disables Storage Hierarchy	Disabled, ★ Enabled
Endorsement Hierarchy	Enables or Disables Endorsement Hierarchy	Disabled, ★ Enabled
TPM 2.0 UEFI Spec Version	Select the TCG2 Spec Version Support, TCG_1_2: the Compatible mode for Win8/Win10 TCG_2: Support new TCG2 protocol and event format for Win10 or later	TCG_1_2, ★ TCG_2
Physical Presence Spec Version	Select to Tell O.S. to support PPI Spec Version 1.2 or 1.3. Note some HCK tests might not support 1.3.	1.2, ★ 1.3
Device Select	TPM 1.2 will restrict support to TPM 1.2 devices, TPM 2.0 will restrict support to TPM 2.0 devices, Auto will support both with the default set to TPM2.0 devices if not found, TPM 1.2 devices will be enumerated	TPM1.2, TPM2.0, ★ Auto

● Super IO Configuration

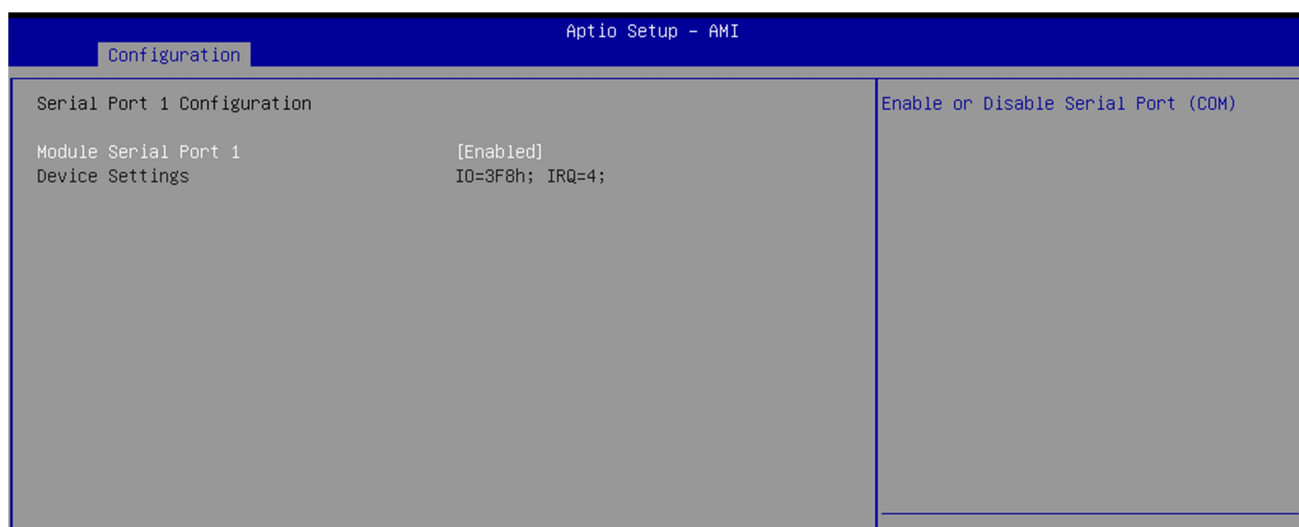
System Super IO Chip Parameters



Feature	Description	Options
Watch Dog Timer	Enable/Disable Watch Dog Timer	★ Disabled, Enabled
Watch Dog Timer [Enable]		
Timer Unit	Select Timer count unit of WDT	★ Second, Minute
Timer value	Set WDT Timer value seconds/minutes	★ 20

● Serial Port 1 Configuration

Set Parameters of Serial Port 1



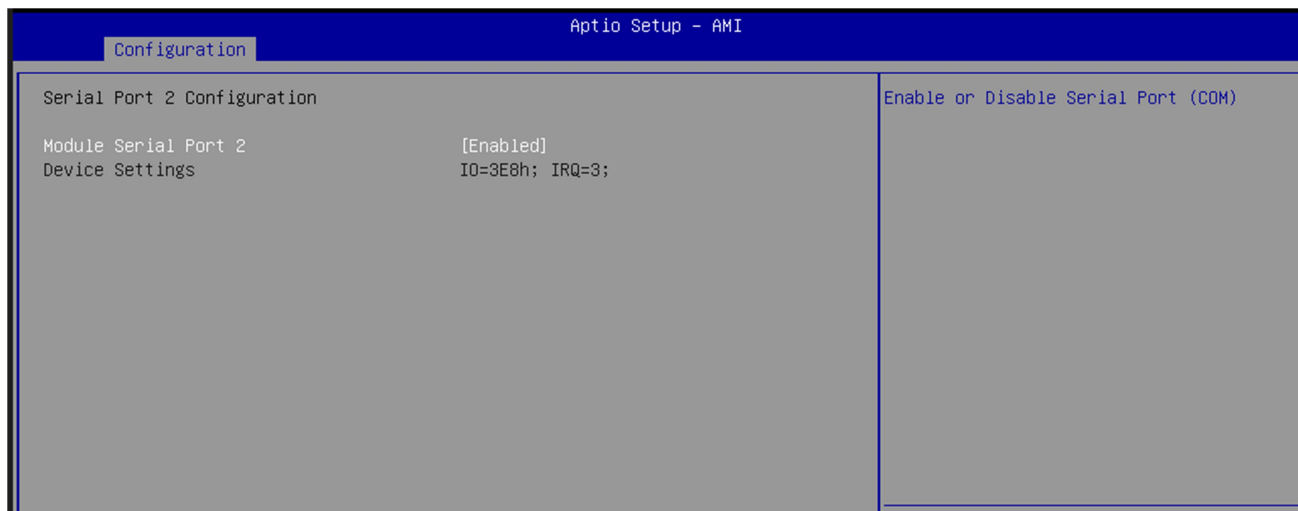
Feature	Description	Options
Serial Port 1	Enable or Disable Serial Port (COM)	★ Enabled, Disabled

● Serial Port 2 Configuration

Set Parameters of Serial Port 2

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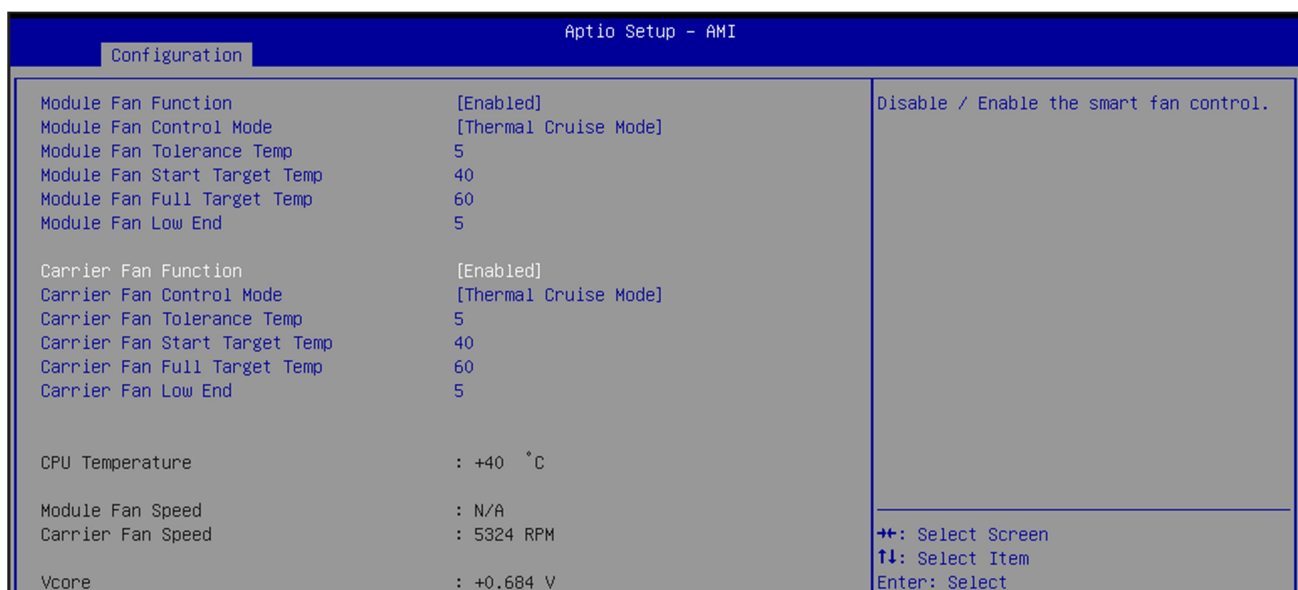
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Feature	Description	Options
Serial Port 2	Enable or Disable Serial Port (COM)	★ Enabled, Disabled

● H/W Monitor

Monitor hardware status



Feature	Description	Options
Module Fan Function	Enable/Disable the smart fan control	★ Disabled, Enabled
Module Fan Function [Enable]		
Module Fan Control Mode	Smart Fan Mode Select	★ Thermal Cruise Mode, Fan Control Mode
Module Fan Tolerance Temp	In Thermal Cruise Mode: Tolerance of Target Temperature	★ 5
Module Fan Start Target Temp	In Thermal Cruise Mode: Start Temperature	★ 40
Module Fan Full Target Temp	In Thermal Cruise Mode: Full Speed Temperature	★ 60
Module Fan Low End	In Thermal Cruise Mode: Low end of fan speed (0~100%)	★ 5
Carrier Fan Function	Enable/Disable the smart fan control	★ Disabled, Enabled
Carrier Fan Function [Enable]		

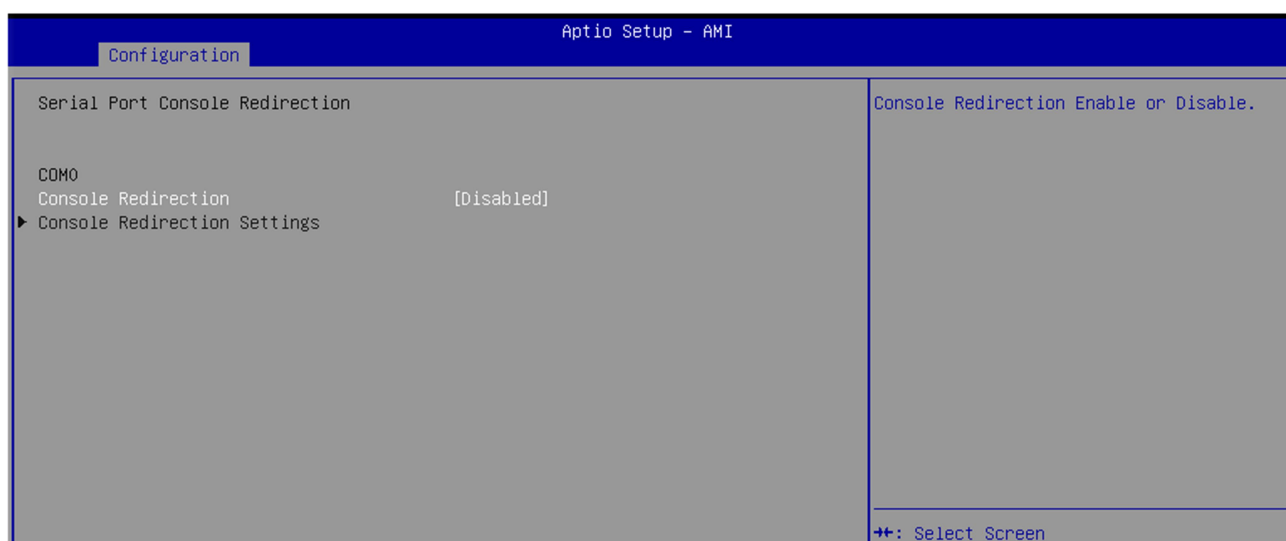
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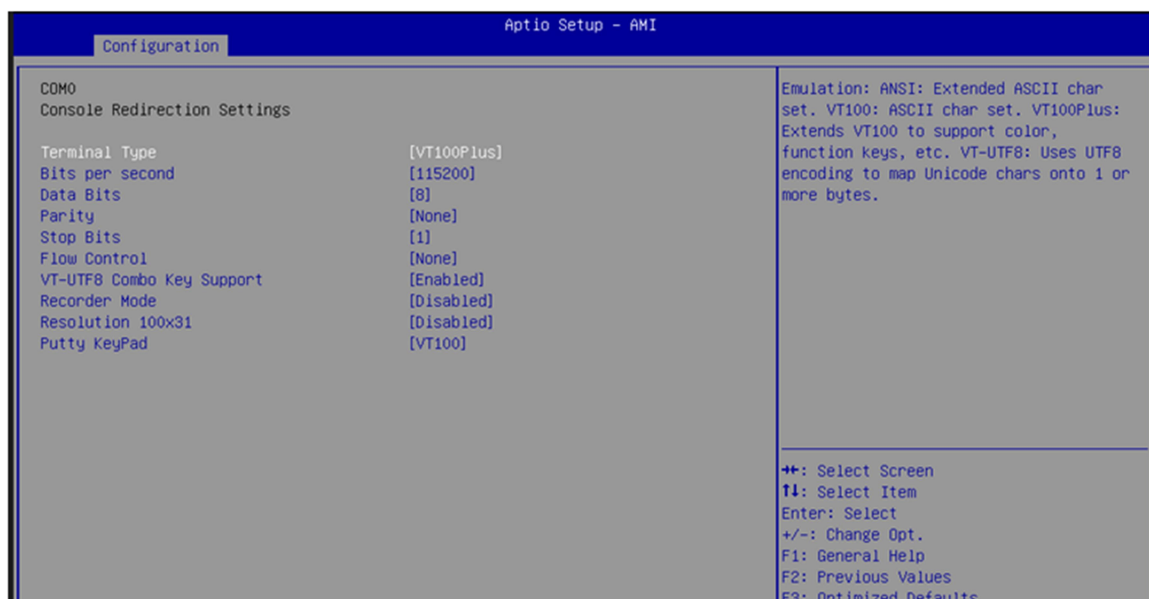
Carrier Fan Control Mode	Smart Fan Mode Select	★ Thermal Cruise Mode, Fan Control Mode
Carrier Fan Tolerance Temp	In Thermal Cruise Mode: Tolerance of Target Temperature	★ 5
Carrier Fan Start Target Temp	In Thermal Cruise Mode: Start Temperature	★ 40
Carrier Fan Full Target Temp	In Thermal Cruise Mode: Full Speed Temperature	★ 60
Carrier Fan Low End	In Thermal Cruise Mode: Low end of fan speed (0~100%)	★ 5
Module Fan Function	Enable/Disable the smart fan control	★ Disabled, Enabled

● Serial Port Console Redirection

Serial Port Console Redirection



Feature	Description	Options
Console Redirection	Console Redirection Enable or Disable	★ Disabled, Enabled



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● COM0 Console Redirection Settings

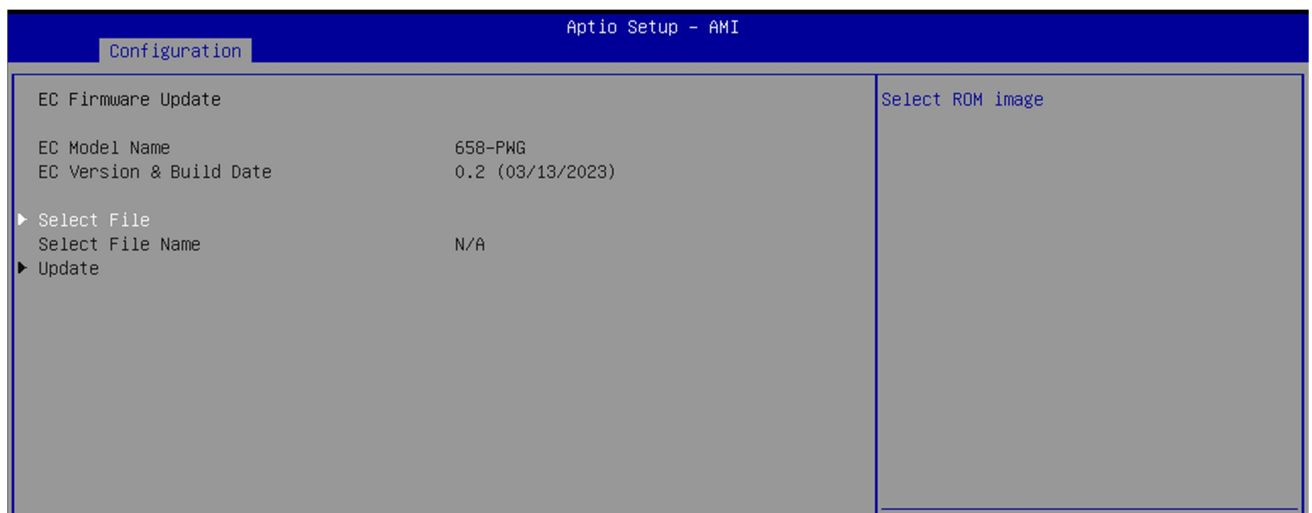
Feature	Description	Options
Terminal Type	Emulation: ANSI: Extended ASCII char set. VT100: ASCII char set. VT100+: Extends VT100 to support color , function keys, etc. VT-UTF8: Uses UTF8 encoding to map Unicode chars onto 1 or more bytes.	VT100,★ VT100Plus, VT-UTF8, ANSI
Bits per second	Select Serial port transmission speed. The speed must be matched on other side. Long or noisy lines may require lower speeds.	★ 115200, 9600, 19200, 38400, 57600
Data bits	Data bits	★ 8, 7
Parity	A parity bit can be sent with the data bits to detect some transmission errors. Even: parity bit is 0 if the num of 1's in the data bits is even. Odd: parity bit is 0 if num of 1's in the data bits is odd. Mark: parity bit is always 1. Space parity bit is always 0. Mark and Space Parity do not allow for error detection. They can be used as an additional data bit.	★ None, Even, Odd, Mark, Space
Stop Bits	Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.	★ 1,2
Flow Control	Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signal.	★ None, Hardware RTS/CTS
VT-UTFB Combo Key Support	Enable VT-UTF8 Combination Key Support for ANSI/VT100 terminals	★ Enabled, Disabled
Recorder Mode	With this mode enabled only text will be sent. This is to capture Terminal data.	★ Disabled, Enabled
Resolution 100x31	Enables or disables extended terminal resolution	★ Disabled, Enabled
Putty KeyPad	Select FunctionKey and KeyPad on Putty	★ VT100, LINUX,XTERMR6, SCO,ESCN,VT400

● EC Firmware Update

EC Firmware Update

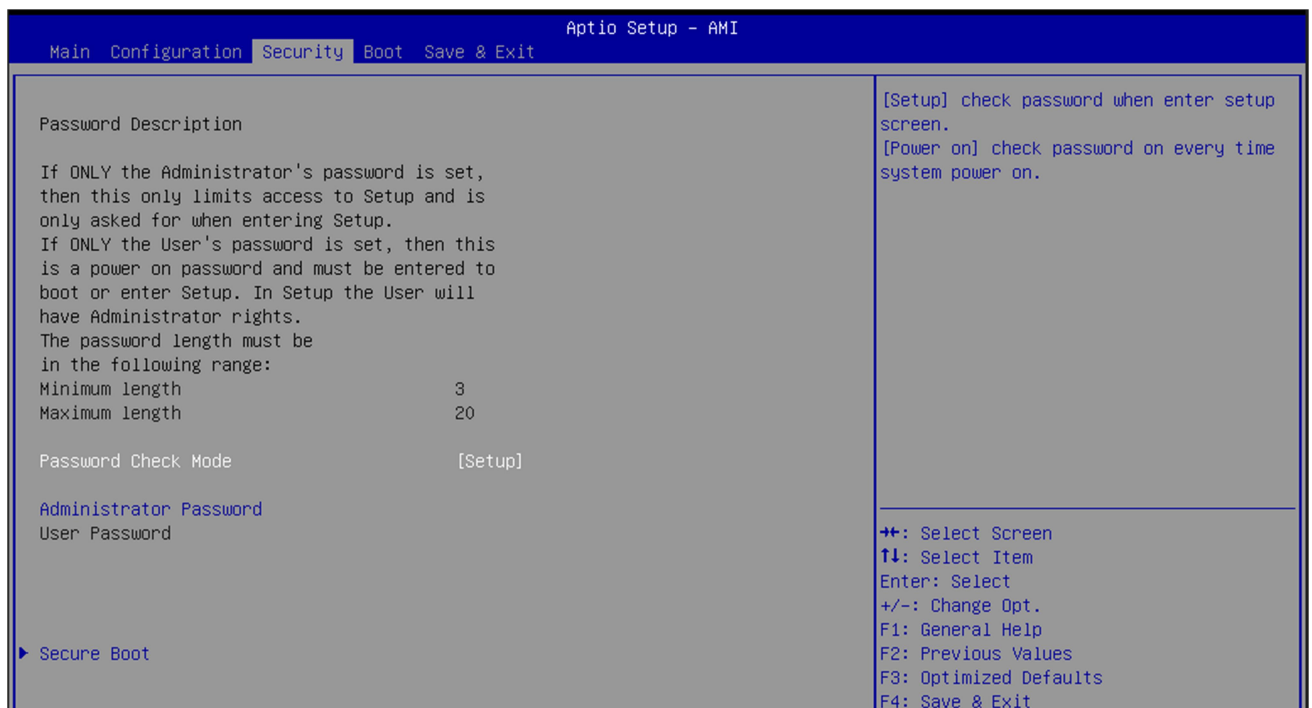
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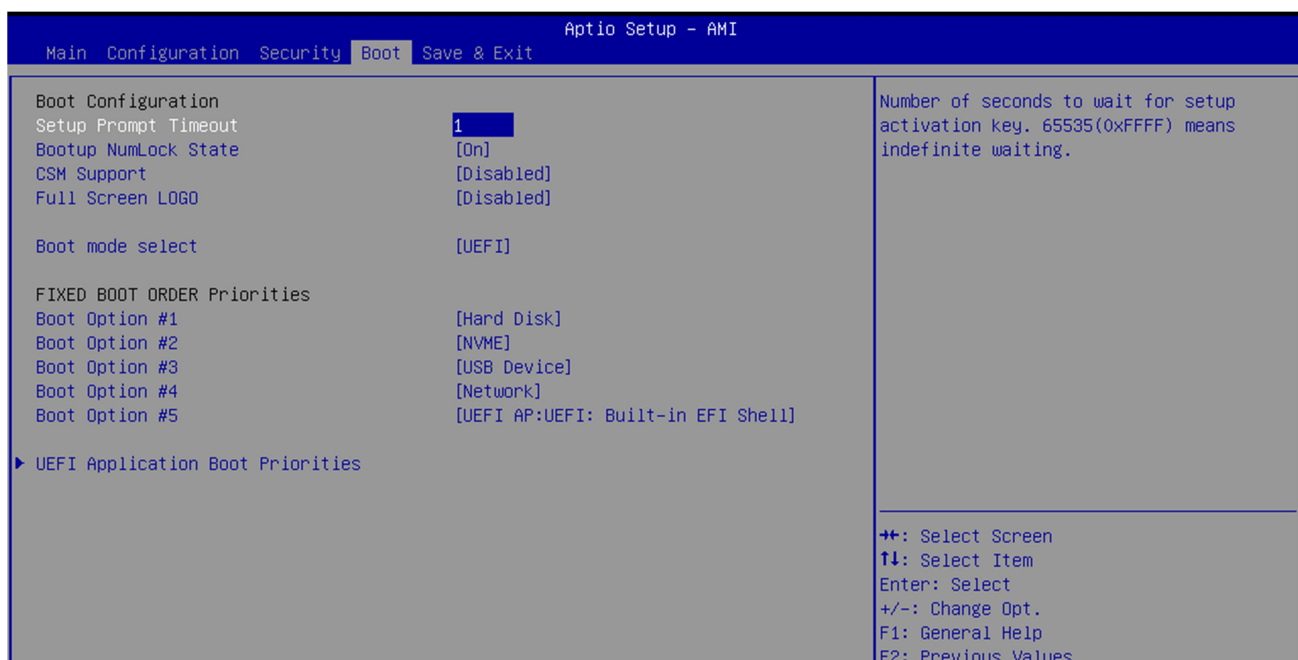
Feature	Description	Options
Select File	Select ROM image	

3.5 Security



Feature	Description	Options
Password Check Mode	[Setup] check password when enter setup screen. [Power on] check password on every time system power on.	★ Setup, Power on
Administrator Password	Set Administrator Password	

3.6 Boot

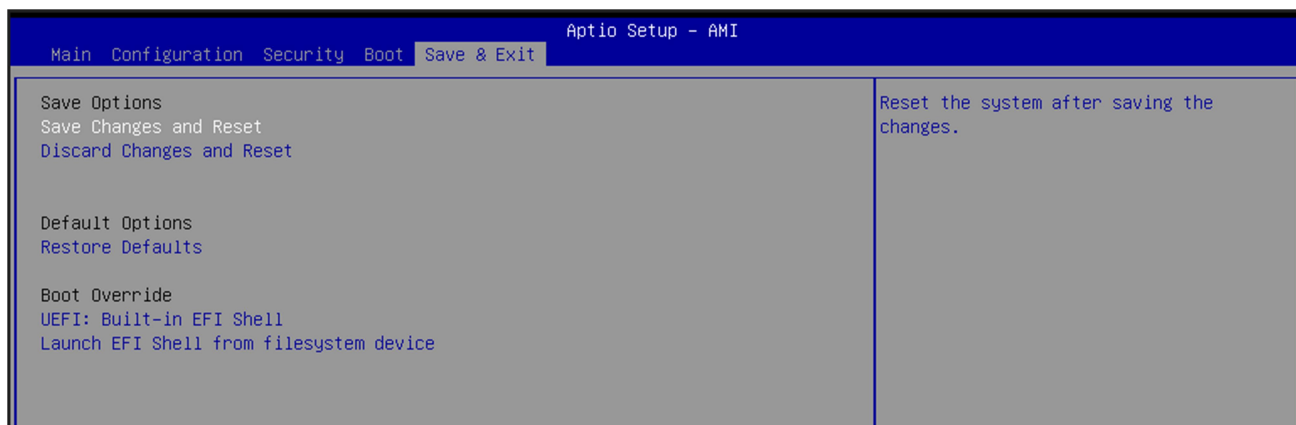


Feature	Description	Options
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.	★ 1
Bootup NumLock State	Select the keyboard NumLock state	★ On, Off
CSM Support	Enable/Disable CSM support	★ Disabled
Full Screen LOGO	Enables or disables Quiet Boot option and Full screen Logo.	★ Disabled, Enabled
Boot mode select	Select boot mode LEGACY/UEFI	★ UEFI ,Legacy
Boot Option #1~5	Sets the system boot order	★ Hard Disk, NVME, USB Device, Network, UEFI AP: UEFI: Built-in EFI Shell, Disabled
UEFI Application Boot Priorities	Specifies the Boot Device Priority sequence from available UEFI Application	

3.7 Save & Exit

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Feature	Description	Options
Save Changes and Reset	Reset the system after saving the changes.	
Discard Changes and Reset	Reset system setup without saving any changes.	
Restore Defaults	Restore/Load Default values for all the setup options.	
UEFI: Built-in EFI Shell	Reset the system after saving the changes. (Boot option filter: UEFI only)	
Launch EFI Shell from filesystem device	Attempts to Launch EFI Shell application (Shell.efi) from one of the available filesystem devices.	

4 CHAPTER 4: AMI BIOS UTILITY

MB only support BIOS/EC update under UEFI shell environment, refer the following step, please.

● BIOS update

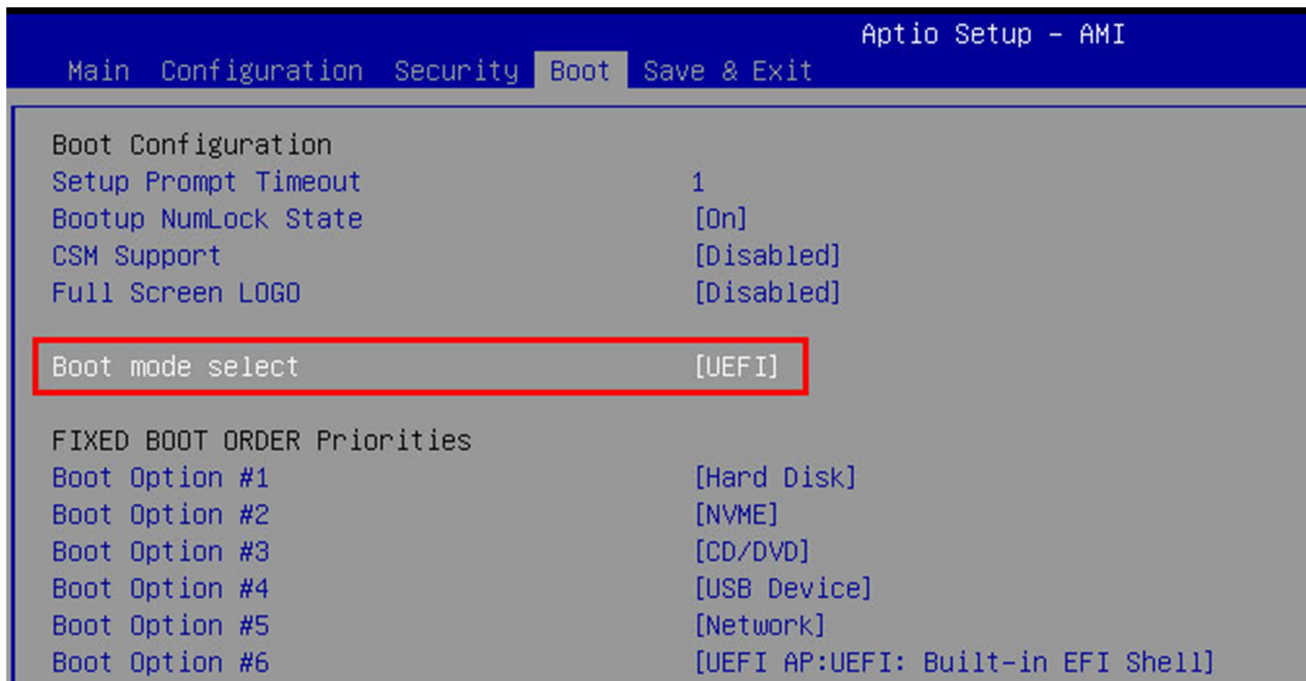
Step 1. Unzip update file to the USB DOK (USB DOK must be FAT or FAT32 format)

Step 2. Make sure "Boot mode select" item is "UEFI" in the BIOS "Boot" page

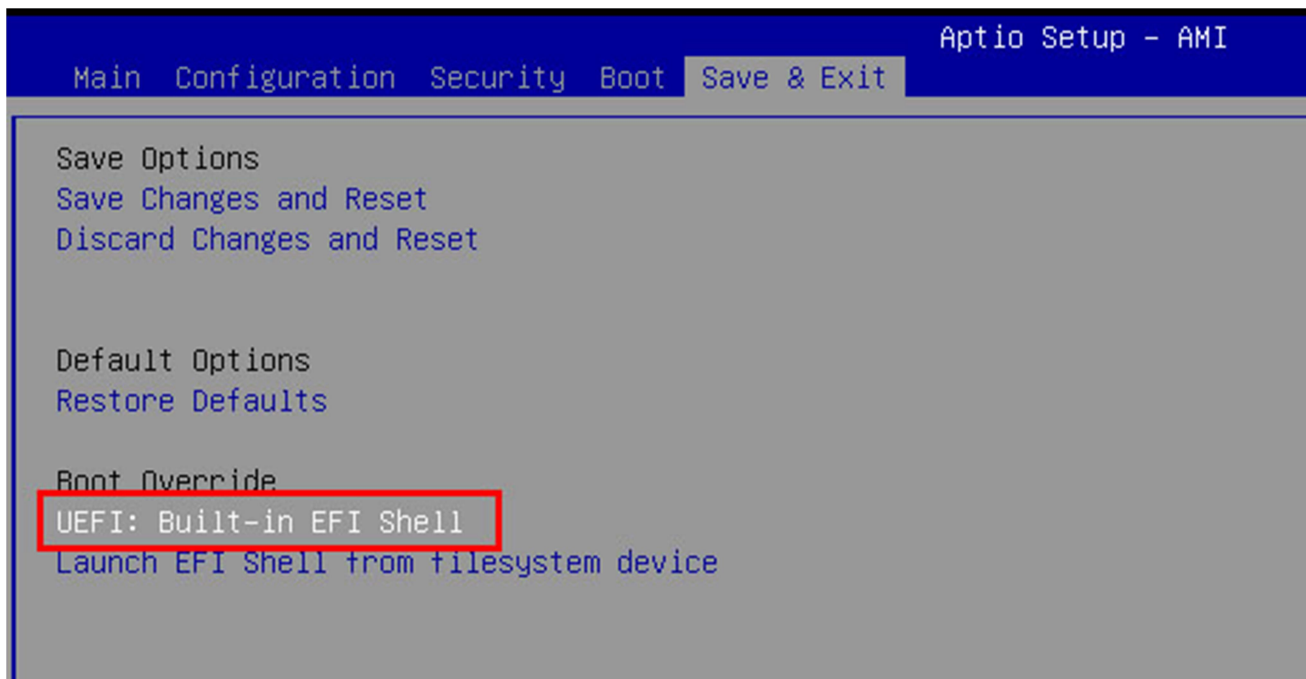
Shown as below picture

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Step 3. Plug the USB DOK on the target system and select "Built-in EFI Shell" in the BIOS "Save&Exit" page Shown as below picture



Step 4. Under the UEFI shell, change prompt to your USB DOK, the below example is "fs0:"

Step 5. Then change the folder with updated file and use command: "update" and press enter

```
FS0:\> cd Update_FPT_PCOM-B657VGL_0_0_16
FS0:\Update_FPT_PCOM-B657VGL_0_0_16\> Update.efi_
```

Step 6. The updating process will start and show the updating progress

Step 7. Please power off and restart the system once updating finished

```
EDK II
UEFI v2.70 (American Megatrends, 0x00050013)
Mapping table
  FS0: Alias(s):HD0r0b:;BLK1:
        PciRoot(0x0)/Pci(0x14,0x0)/USB(0x11,0x0)/HD(1,MBR,0x6A4499BF,0x800,0x1
D6B800)
  BLK0: Alias(s):
        PciRoot(0x0)/Pci(0x14,0x0)/USB(0x11,0x0)
Intel (R) Flash Programming Tool Version: 15.0.30.1776
Copyright (C) 2005 - 2021, Intel Corporation. All rights reserved.

Reading HSFSTS register... Flash Descriptor: Valid

--- Flash Devices Found ---
ID:0xC22019   Size: 32768KB (262144Kb)

GbE Region does not exist.

- Erasing Flash Block [0x2000000] - 100 percent complete.
- Programming Flash [0x2000000] 32768KB of 32768KB - 100 percent complete.
RESULT: The data is identical.32768KB of 32768KB - 100 percent complete.

FPT Operation Successful.

FS0:\Update_FPT_PCOM-B657VGL_0_0_16\> _
```

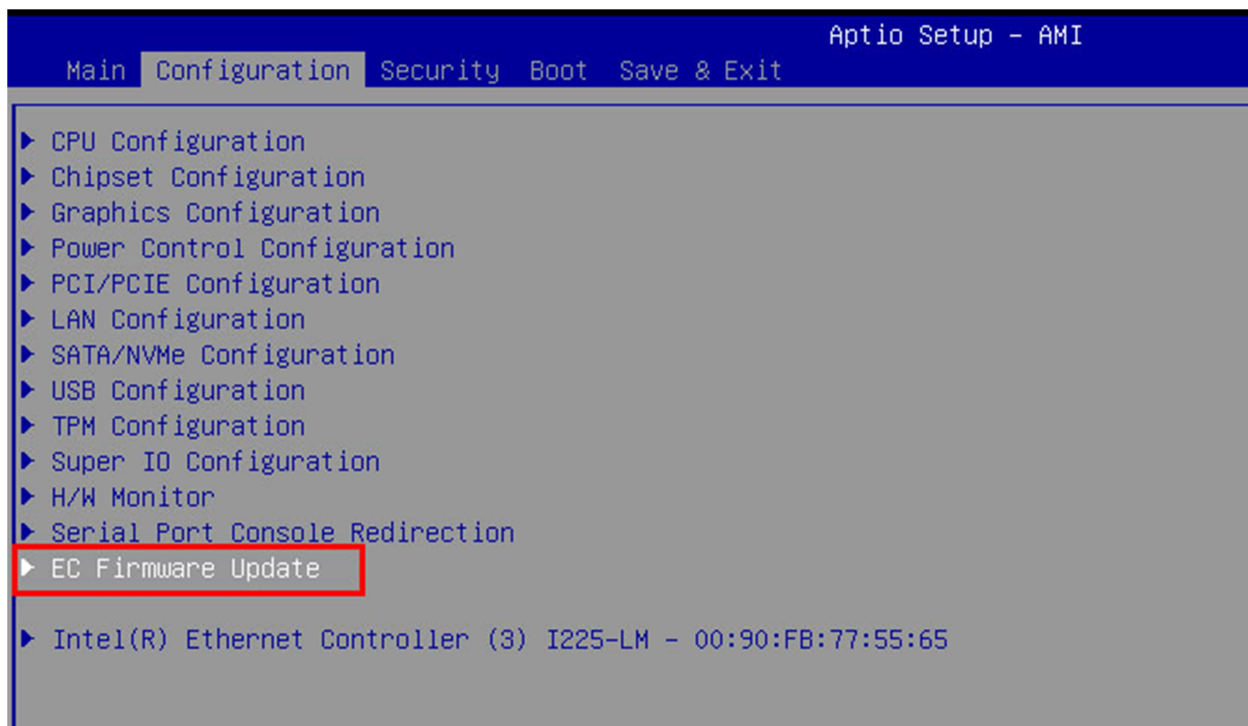
● EC update

Step 1. Unzip EC binary file to the USB DOK (USB DOK must be FAT or FAT32 format)

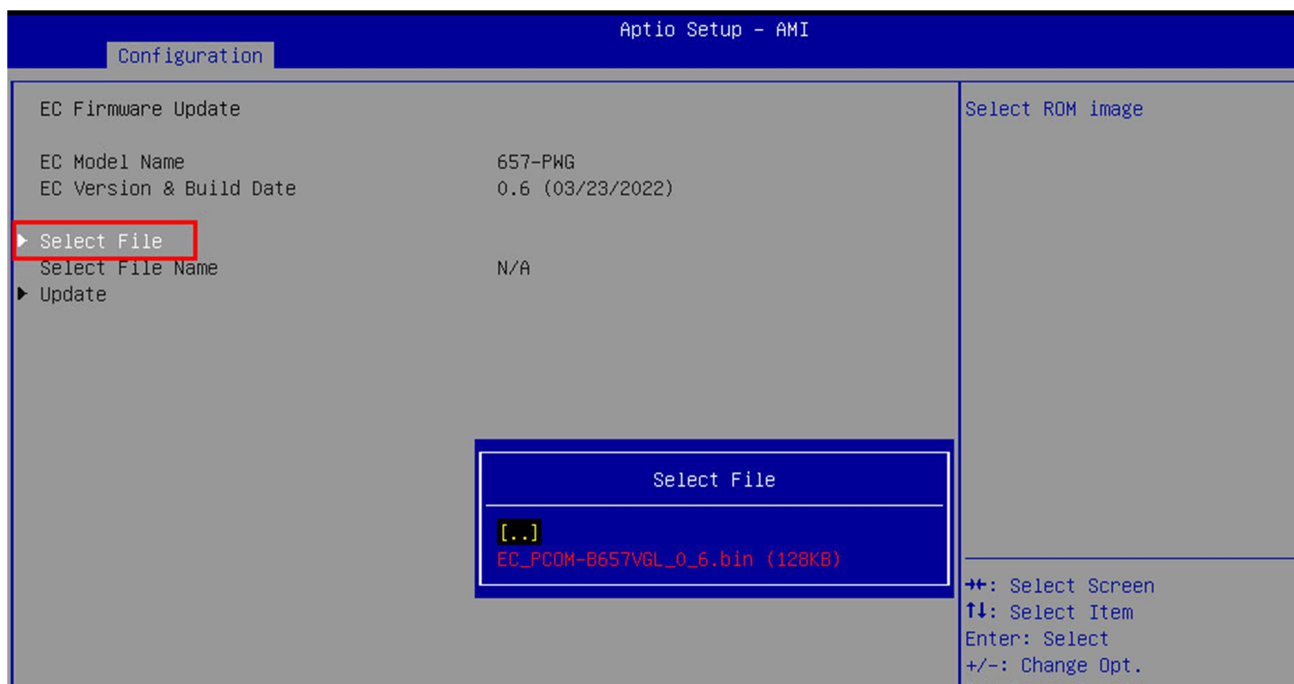
Step 2. Select "EC Firmware Update" item in BIOS setup menu

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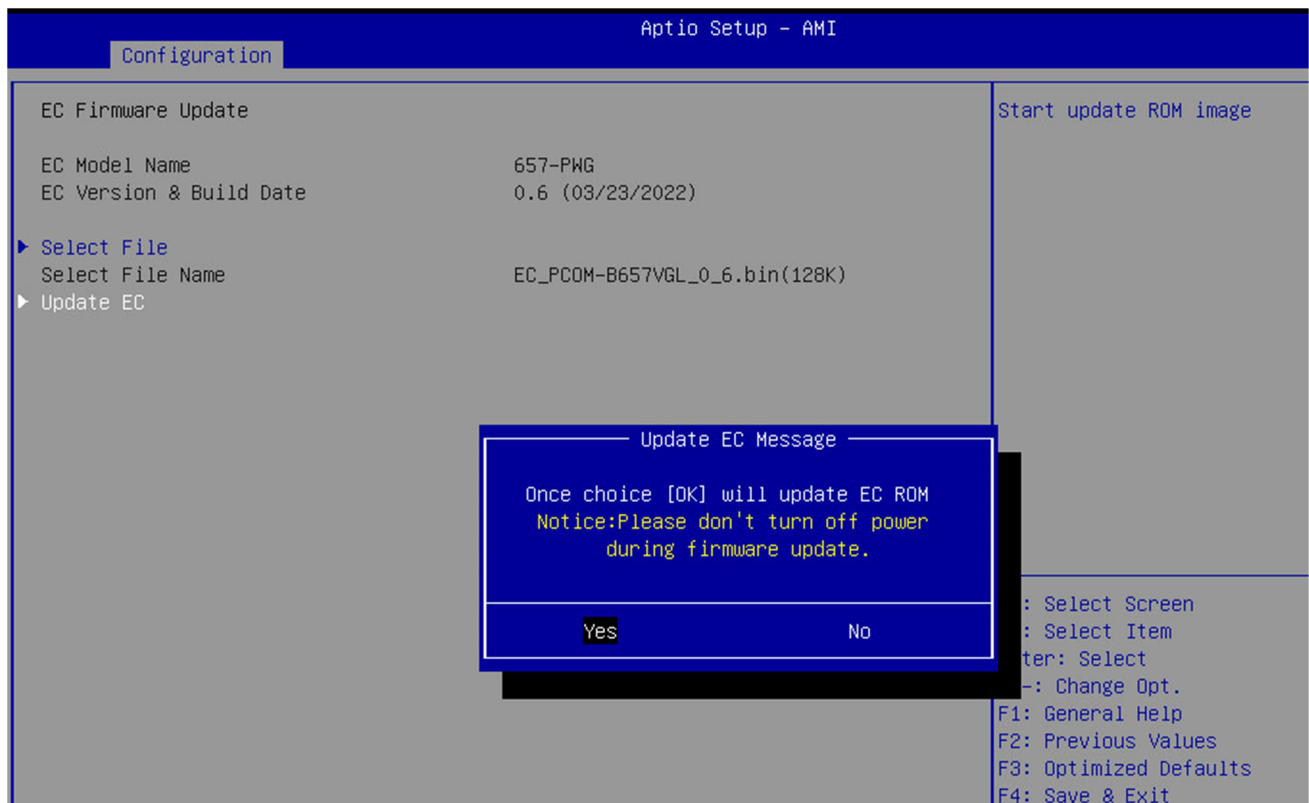
Step 3. Select EC binary file by option item shown as below



Step 4. Select "Yes" to start EC update (Please don't turn off power during firmware update)

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Step 5. Turn off power to make system into G3 status once updating finished, then power on the system

